

THE UNIVERSITY of NORTH CAROLINA at CHAPEL HILL



MAJORANA DEMONSTRATOR Simulations (Geometry)

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1/19/10

Henning -- MaGe Status

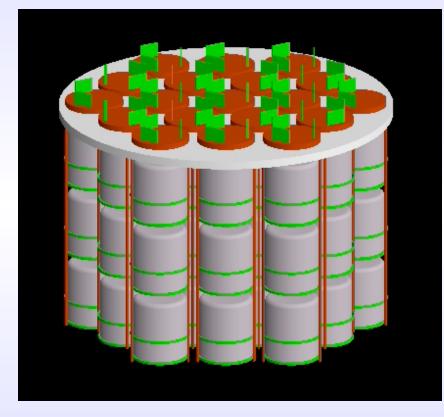
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Outline

- Previous Work
- The problem: The DEMONSTRATOR Geometry
- Coding Issues

MAJORANA "Reference Design" Simulation (2006)

Simulated Geometry Shields & Cryostat Removed



Simulation Includes:

- 57 Enriched crystal w/ deadlayers.
- LFEPs
- Support Rods
- Ge Trays
- Contact Rings
- Cryostat
- Shields:
 - Inner, Outer Cu
 - Inner, Outer Pb
 - Neutron shield.
 - Room, rock wall.
- 45,000 CPU hours, 12,000 jobs, 2TB of data.
- Thanks to PDSF:



NATIONAL ENERGY RESEARCH SCIENTIFIC COMPUTING CENTER



Advancing Computational Science of Scale— Producing Real Results

Initial Simulation Campaign based on 'Reference Design'

- Simulate spectrum from sources in all detector components.
- Apply heuristic analysis cuts:
 - Granularity
 - Segmentation.
 - Pulse shape discrimination estimator
 - 3D Reconstruction (highlysegmented detectors).
 - Use clustering of energy deposits
 - Modified electrode

Sources

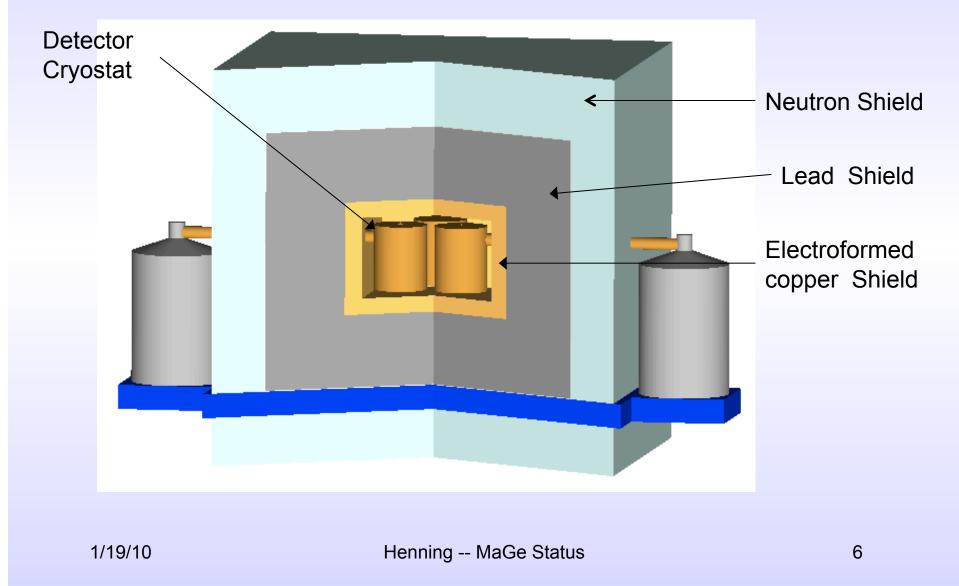
- Crystals Internal:
 - ⁶⁸Ge, ⁶⁰Co, ²¹⁴Bi, ²⁰⁸TI :
 - 2νββ, 0νββ:
- Support Rods: ²⁰⁸TI, ²¹⁴Bi, ⁶⁰Co.
- Ge Trays: ²⁰⁸TI, ²¹⁴Bi.
- Contact Rings: ²⁰⁸TI, ²¹⁴Bi.
- Cabling: ²⁰⁸TI, ²¹⁴Bi.
- LFEPs: ²⁰⁸Tl, ²¹⁴Bi, ⁶⁰Co.
- Cryostat, ²⁰⁸TI, ²¹⁴Bi, ⁶⁰Co:
- Crystals Surface: Rn daughters (alphas). U/Th dust
- Inner Cu shield: ²⁰⁸TI, ²¹⁴Bi, ⁶⁰Co.
- Other Shielding.
- Alphas

Basis for background estimates to date and detector choice.

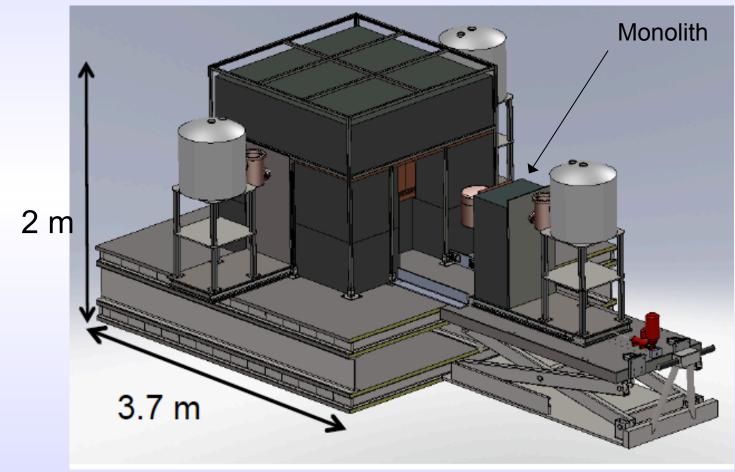
THE DEMONSTRATOR GEOMETRY

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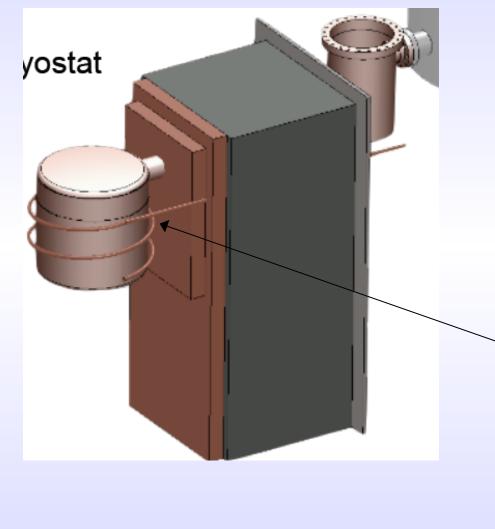
Conceptual Design



Demonstrator Engineering Design

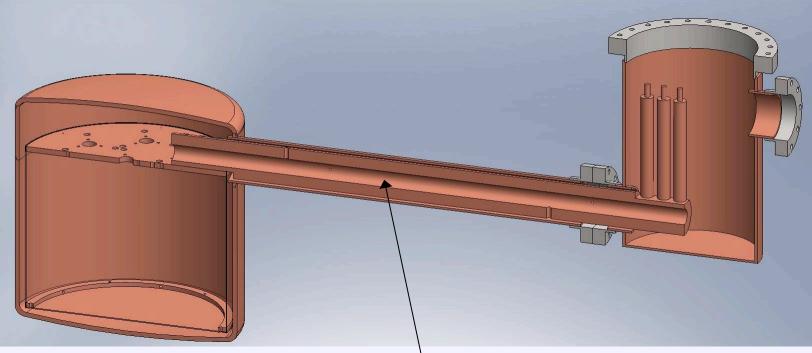


Monolith Design



- Simulation Issues:
 - Shield
 Thickness
 - Cracks in
 Shield
 - Calibration System
 - Contamination in shield

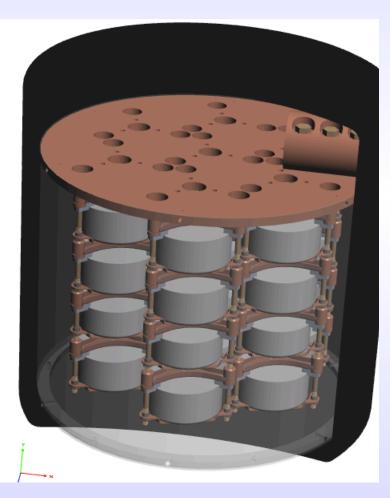
Cryostat and Breakout



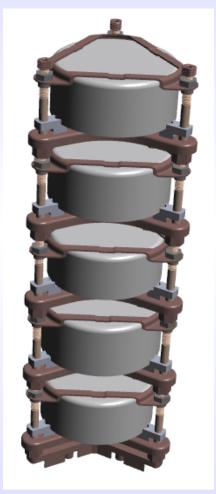
 Shine through cross-arm, contamination in cold-finger support, cables in crossarm, contamination in cryostat, etc.

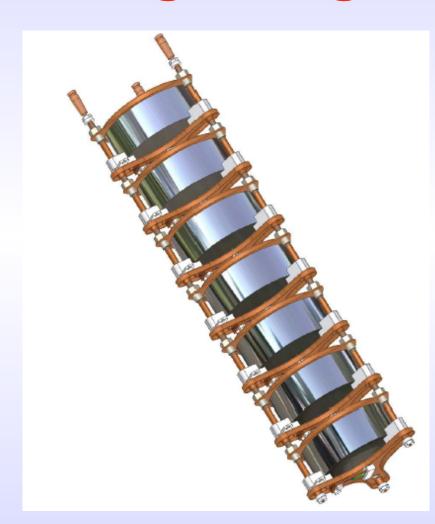
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Module Design



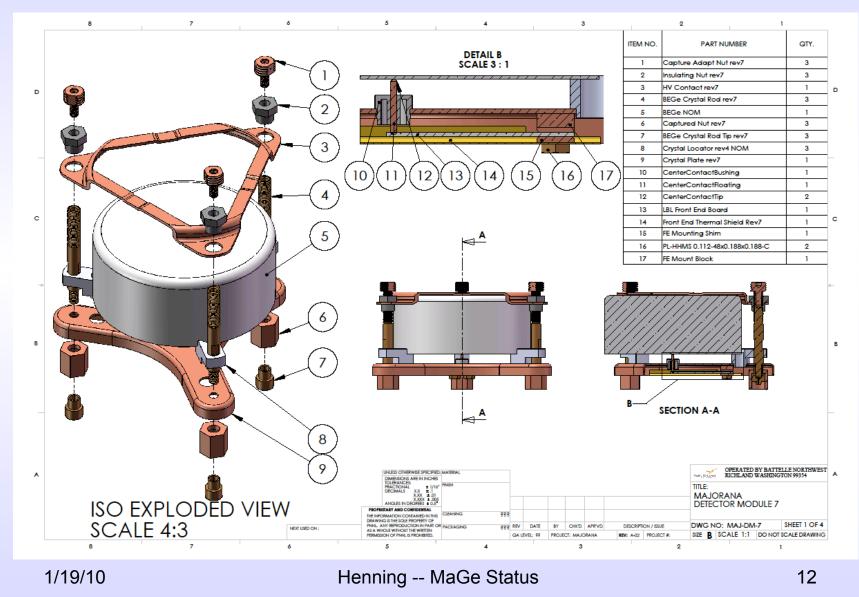
Candidate String Designs



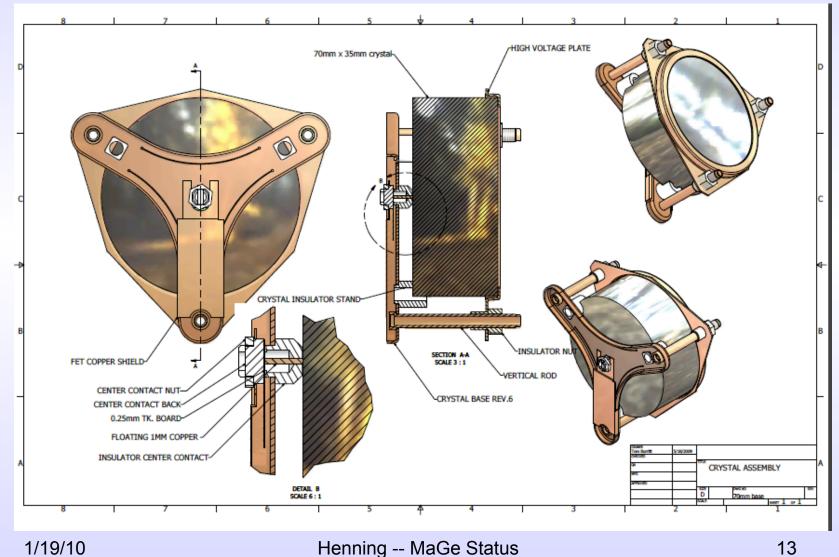


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Candidate Crystal Mount Design (1)



Candidate Mount Design (2)



Conclusions

- LOTS of small parts to simulate in cryostat, many with high fidelity
- Hierarchical vs. flat geometry description
 - Flat easier to implement, but slower. Voxelization improves speed, but requires more memory.
 - Benefit to 64-bit build? Need to verify.
- GDML vs. hard-coding:
 - GDML ~3x slower. Issues with accuracy
 - Engineering dwgs vs. Monte Carlo do not have same priorities.
 - Engineering dwgs. Have mistakes that do not become apparent until construction.
- Geometry at micron scale important at contact. Also most complex part (do'h!) 1/19/10 Henning -- MaGe Status 14

Conclusions (2)

- Implement geometry yesterday. (Next 1-2 months)
- Contend with changing/competing designs.
- Basis for next simulation campaign (Alexis' talk).