

# **Introduction to FPGA Programming**

## **Report of Contributions**

Contribution ID: 1

Type: **not specified**

## **Lesson 01: Digital Systems, FPGAs and HDL**

*Monday, 1 December 2025 09:00 (2 hours)*

Contribution ID: 2

Type: **not specified**

## **Lesson 02: VHDL Fundamentals**

*Monday, 1 December 2025 14:00 (2 hours)*

Contribution ID: 3

Type: **not specified**

## **Lesson 03: Boolean Algebra, Look-up Tables and IOBs**

*Tuesday, 2 December 2025 09:00 (2 hours)*

Contribution ID: 4

Type: **not specified**

## **Lesson 04: Sequential Logic and Flip-Flops**

*Tuesday, 2 December 2025 11:00 (2 hours)*

Contribution ID: 5

Type: **not specified**

## **Lesson 05: Types, Arrays and Arithmetic Functions**

*Wednesday, 3 December 2025 14:00 (2 hours)*

Contribution ID: 6

Type: **not specified**

## **Lesson 06: VHDL Simulation**

*Wednesday, 3 December 2025 16:00 (2 hours)*

Contribution ID: 7

Type: **not specified**

## **Lesson 07: Storing data on FPGAs: RAMs and FIFOs**

*Thursday, 4 December 2025 09:00 (2 hours)*

Contribution ID: 8

Type: **not specified**

## **Lesson 08: Packages, Libraries and Parametetrisation**

*Thursday, 4 December 2025 11:00 (2 hours)*

Contribution ID: 9

Type: **not specified**

## Lesson 09: Finite State Machines

*Thursday, 4 December 2025 14:00 (2 hours)*

Contribution ID: **10**

Type: **not specified**

## **Lesson 10: IP Blocks**

*Friday, 5 December 2025 09:00 (2 hours)*

Contribution ID: 11

Type: **not specified**

## **Lesson 11: Timing on FPGAs**

Contribution ID: 12

Type: **not specified**

## **Lesson 11: External Interfaces**

*Friday, 5 December 2025 11:00 (5 hours)*