

# The DAQ (Data Acquisition) system for CDEX-1

- DAQ requirement
- The design of DAQ for CDEX-1
- DAQ test
- the running status of 20g ULEG & anti-Compton system

# Purpose

This DAQ is for 20g-ULEGGe/1kg-PPCGe & NaI(Tl) anti-Compton

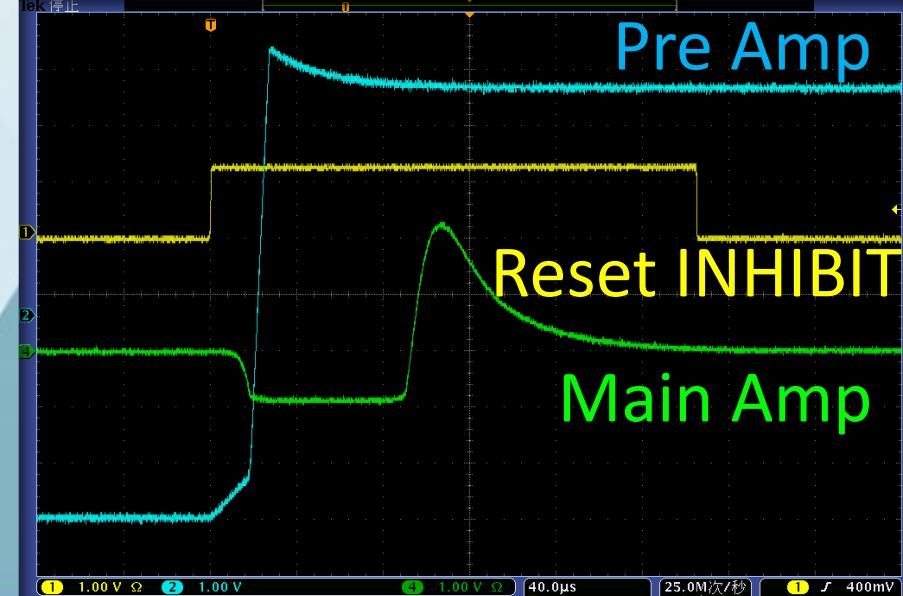
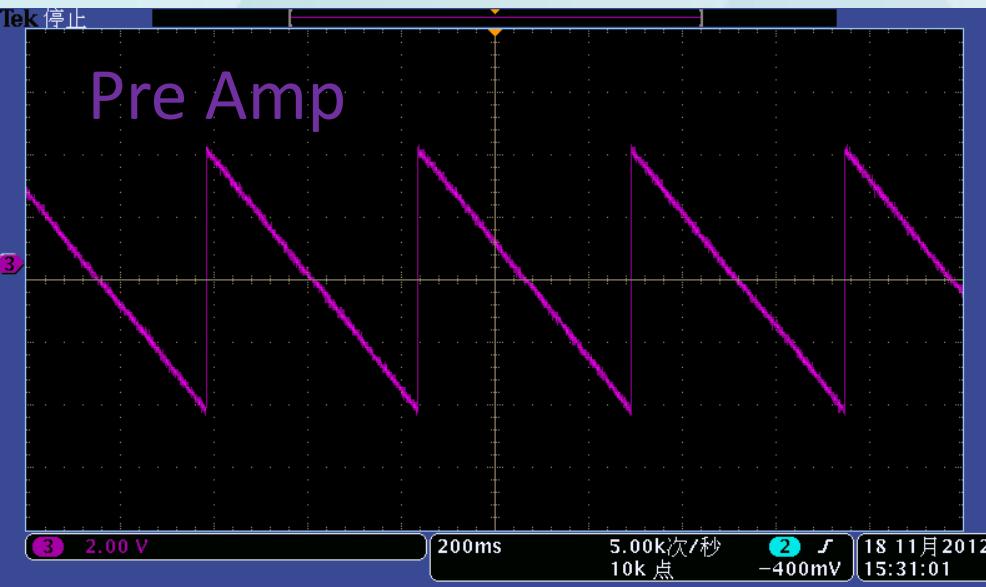
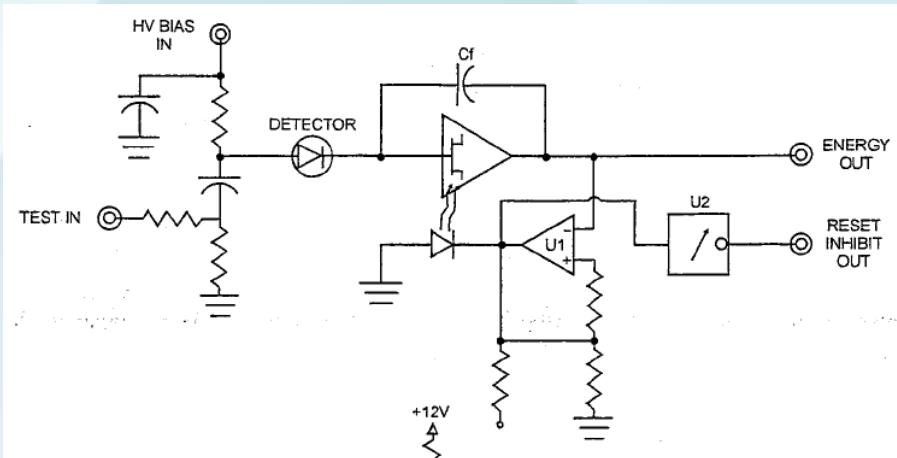
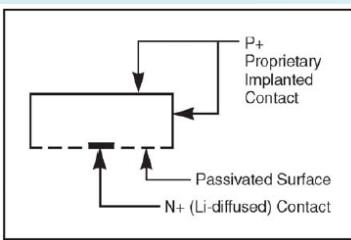
- Record pulse shape
- Record the event time
- Synchronize all the VME modules
- Have a good reliability and stability



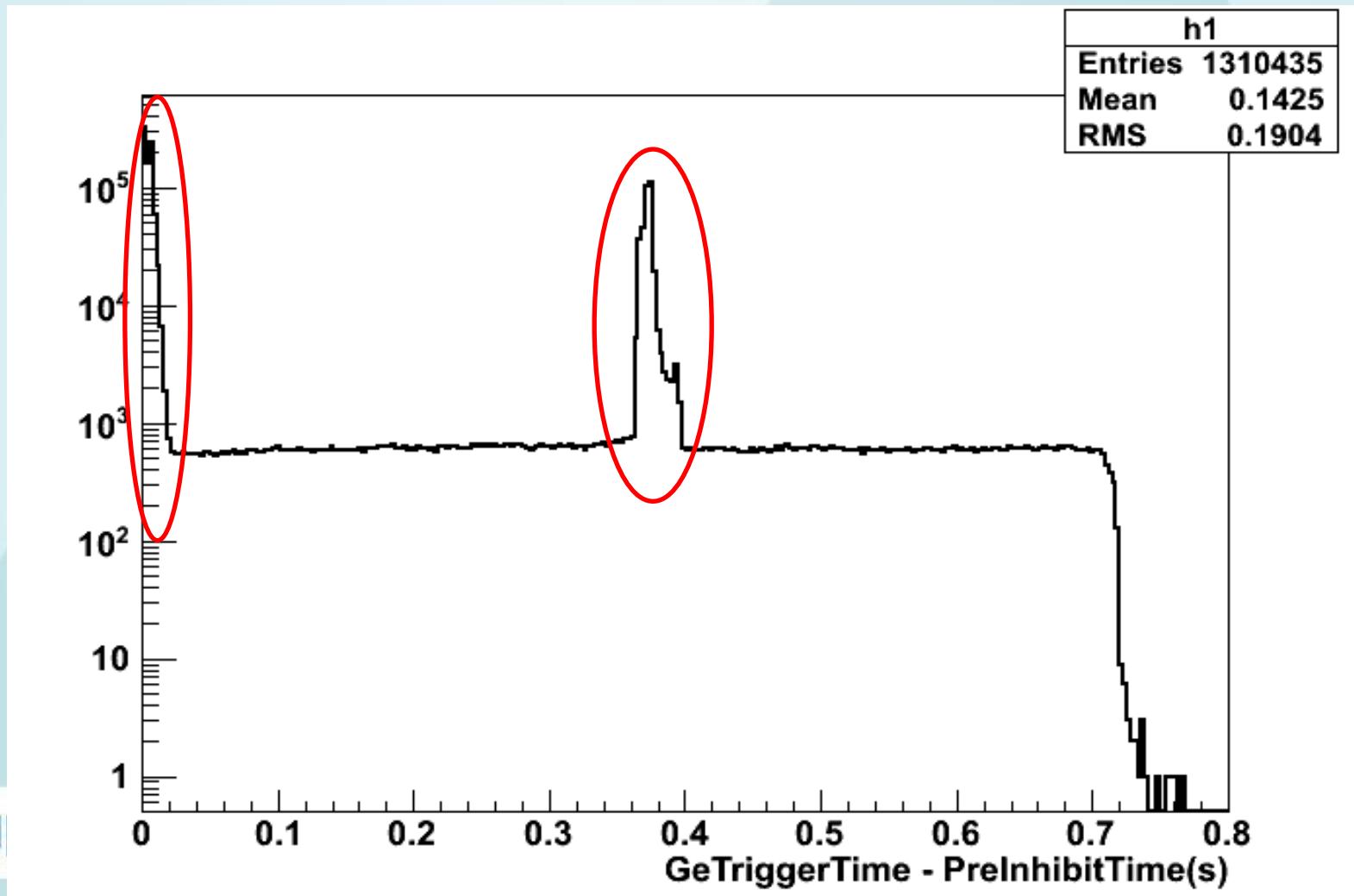
中国锦屏地下实验室  
China Jinping Underground Laboratory

ULEGe: Ultra Low energy threshold Germanium  
PPCGe: P-type Point Contact Germanium

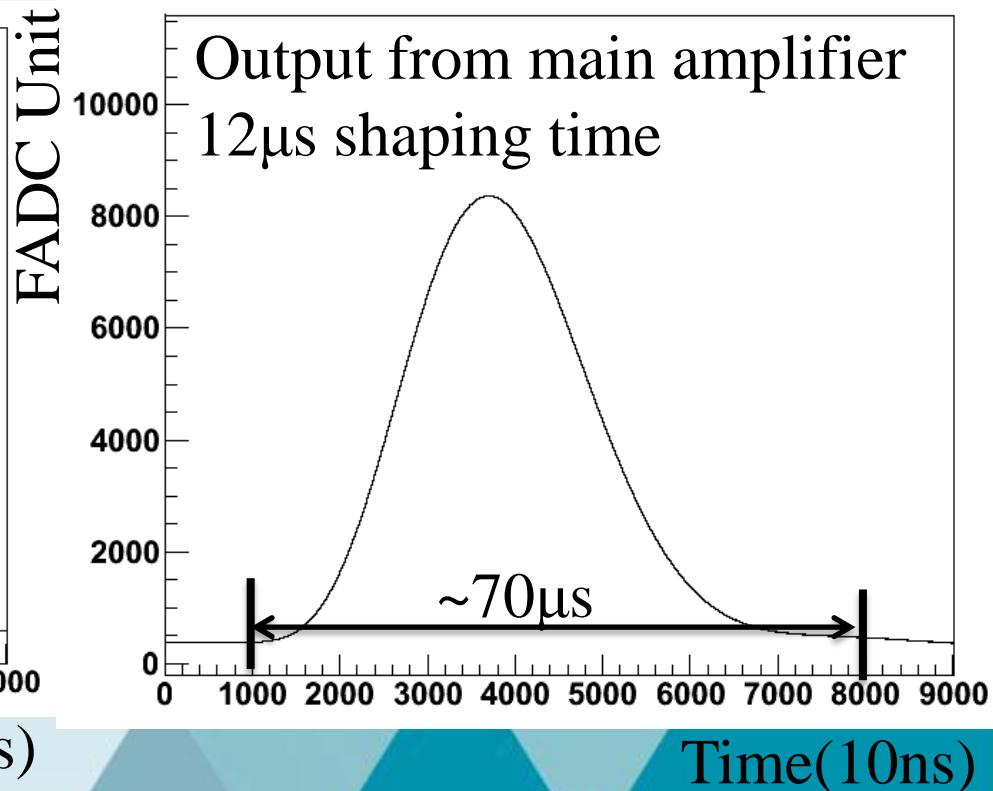
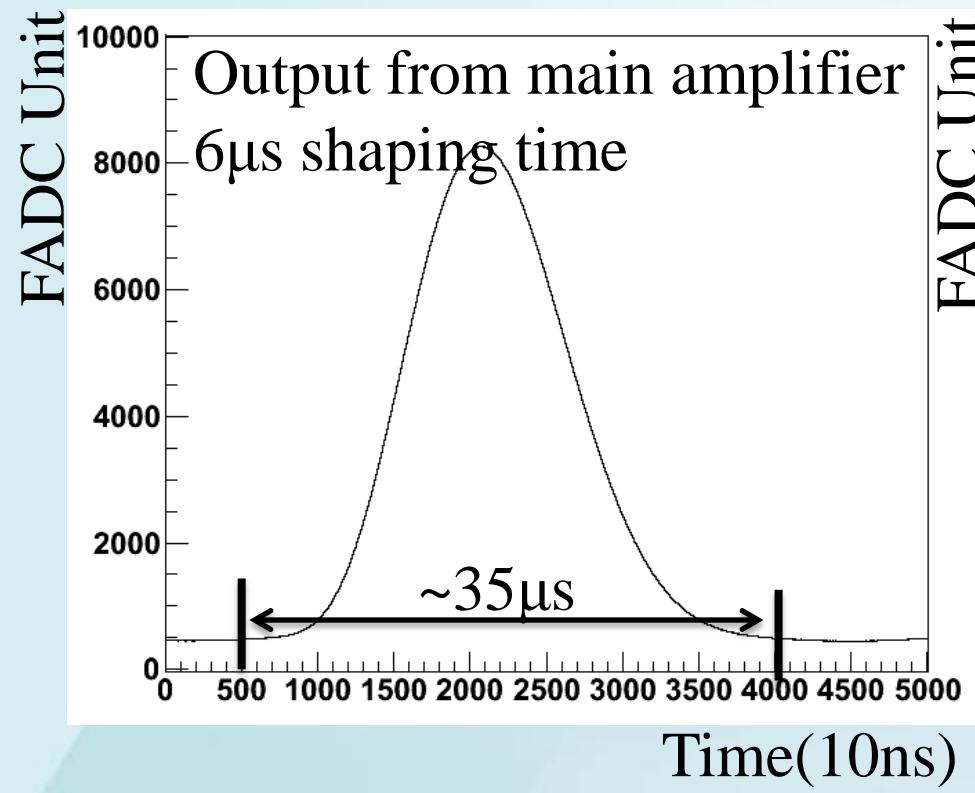
# 20g ULEGGe detector



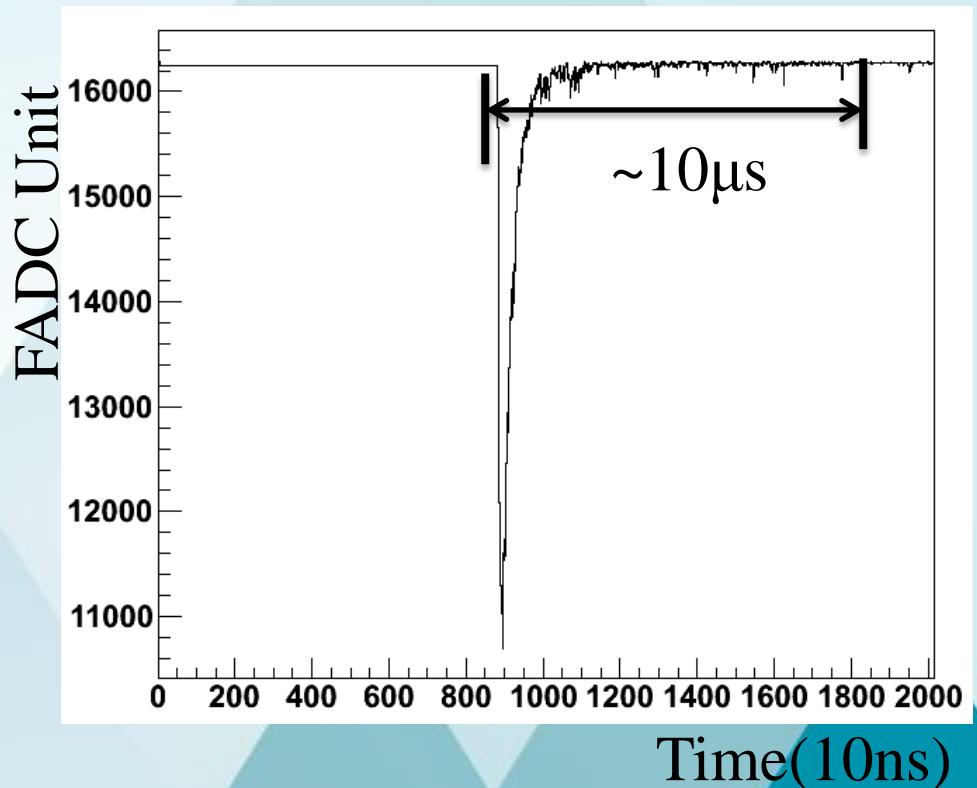
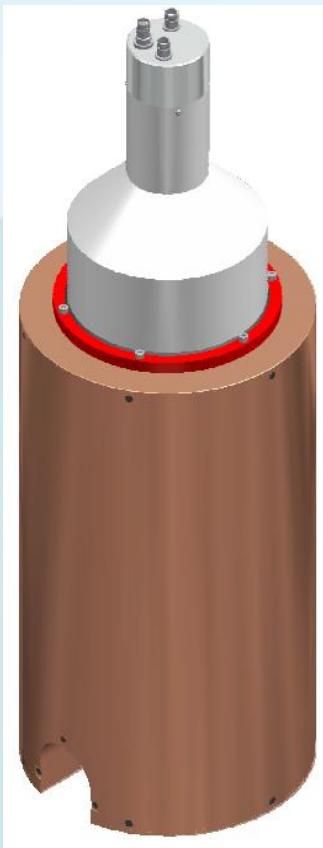
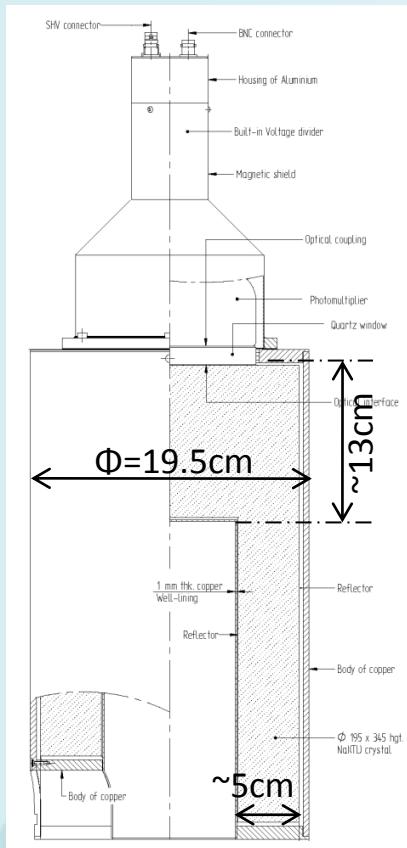
# The time correlation between Ge trigger and Inhibit



# 20g ULEG<sub>e</sub> pulse shape from different shaping time configuration

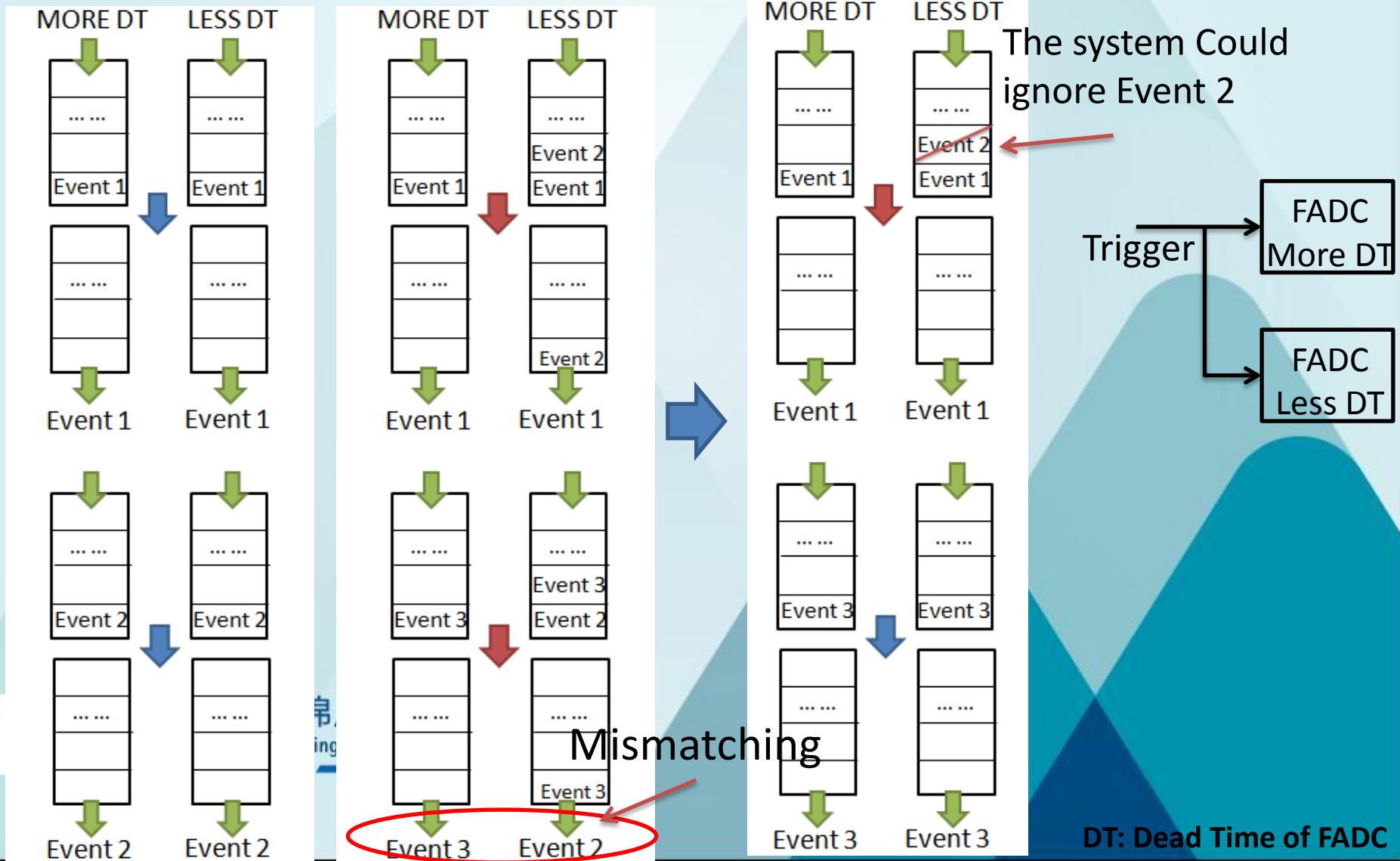


# NaI(Tl) detector



- Weight:  $\sim 40\text{kg}$

# Mismatching phenomenon of different FADCs



# Requirements

- pulse record: need different time window width FADCs
- Record some necessary time: the system trigger, Ge trigger, NaI(Tl) signal, Reset Inhibit
- Synchronize all the modules  
a mechanism must be made to synchronize all the VME modules to record the same event at the same time



中国锦屏地下实验室  
China Jinping Underground Laboratory

# NIM modules

## Amplifier



**CANBERRA 2026**  
Spectroscopy Amplifier  
Gaussian shaping



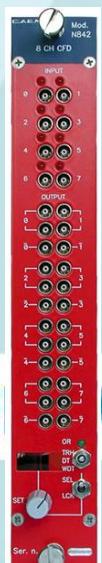
**CANBERRA 2111**  
Timing Filter Amplifier

## Timer



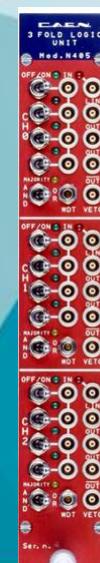
**CAEN N93B**  
Dual Timer

## Discriminator



**CAEN N840/N842**  
Leading edge discriminator

## Logic unit



**CAEN N405**  
3 fold logic unit

CJ

中国锦屏地下实验室  
China Jinping Underground Laboratory

# VME modules

## FADC



**CAEN V1724**

8 channel 14 bit  
100MHz Digitizer

## TDC



**CAEN V1290N**

16 channel Multihit TDC  
52 $\mu$ s full scale range  
25 ps precision  
32k \* 32bit output buffer

## Scaler



**CAEN V560N**

16 Channel scaler  
32 bit deep counting channels  
100MHz counting frequency

## I/O Register



**CAEN V262**

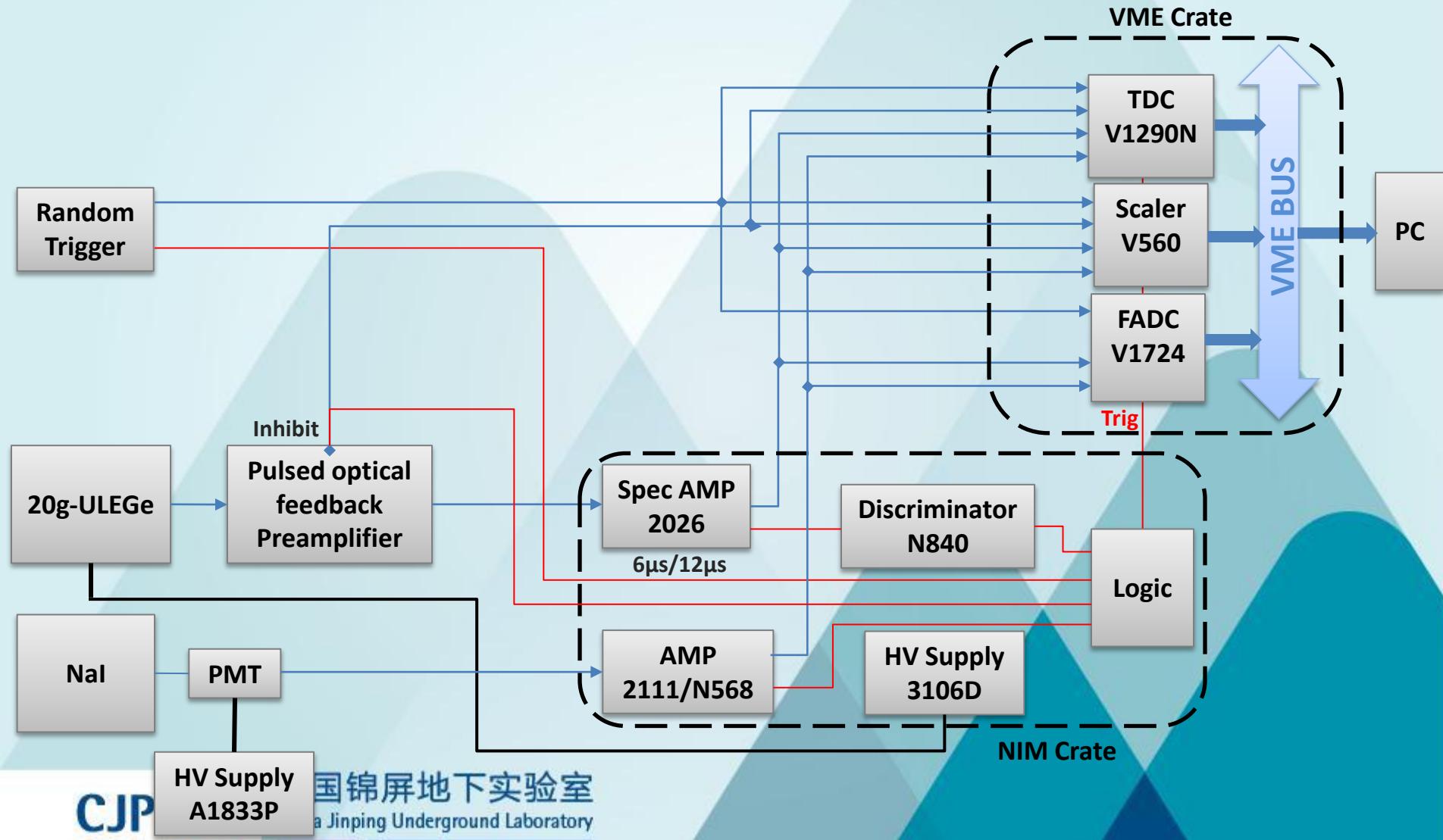
## VME Controller

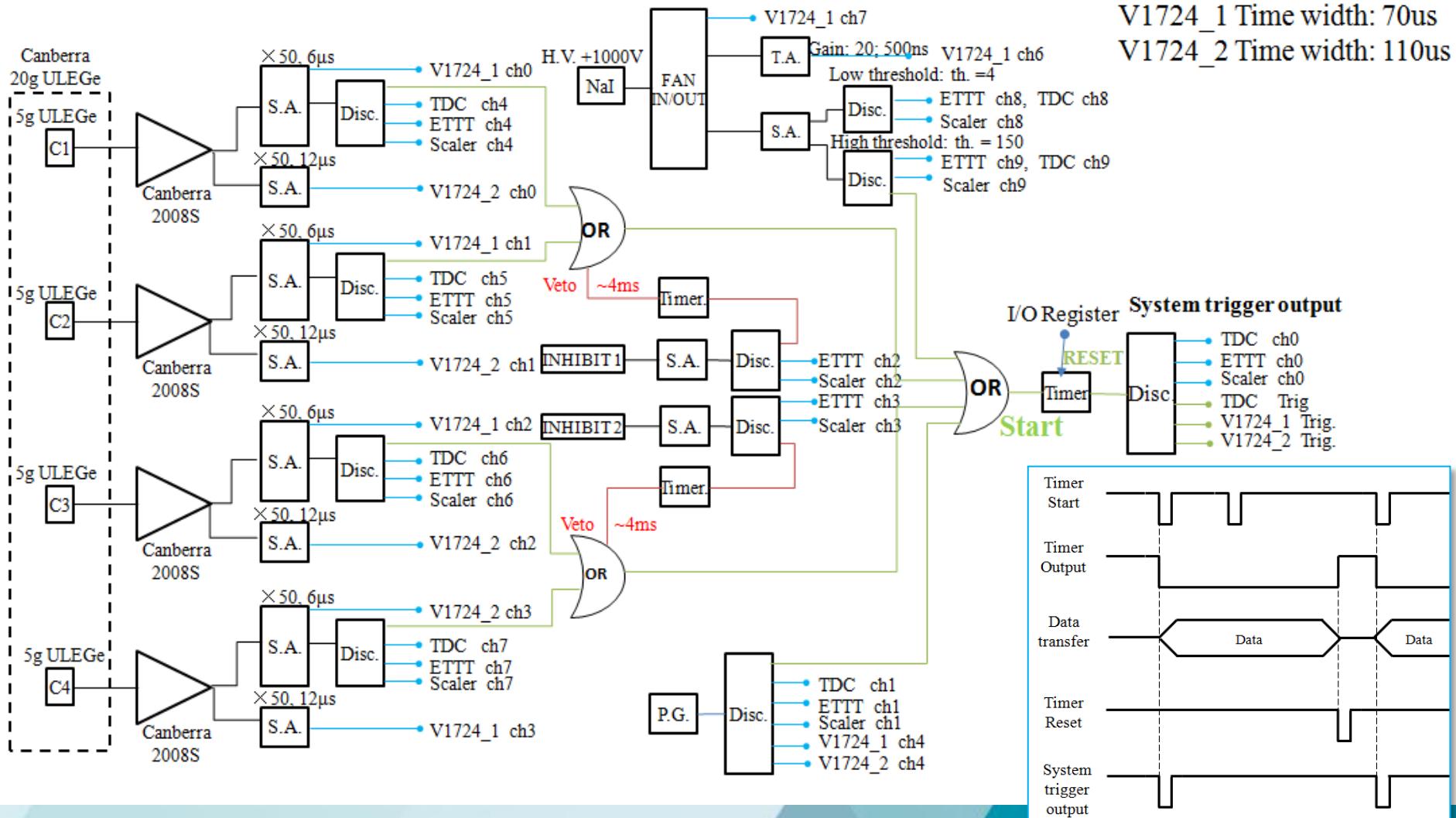


**CAEN V2718**

VME-PCI Optical link  
bridge

# The DAQ for 20g ULEGe with anti-Compton detector





**S.A.: shaping amplifier CANBERRA 2026**

**Disc.: discriminator**

**Timer: N93B**

**P.G.: Pulse generator**

**ETTT: Extended Trigger Time Tag TDC**



**中国锦屏地下实验室**

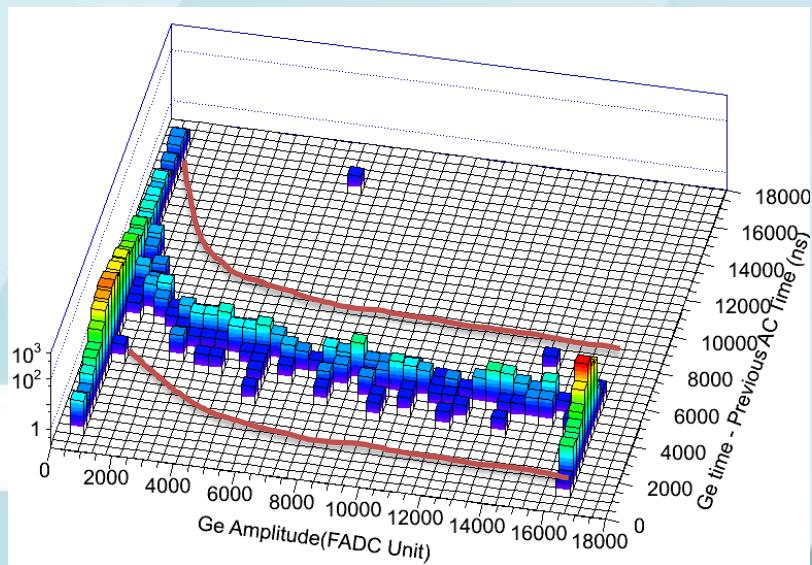
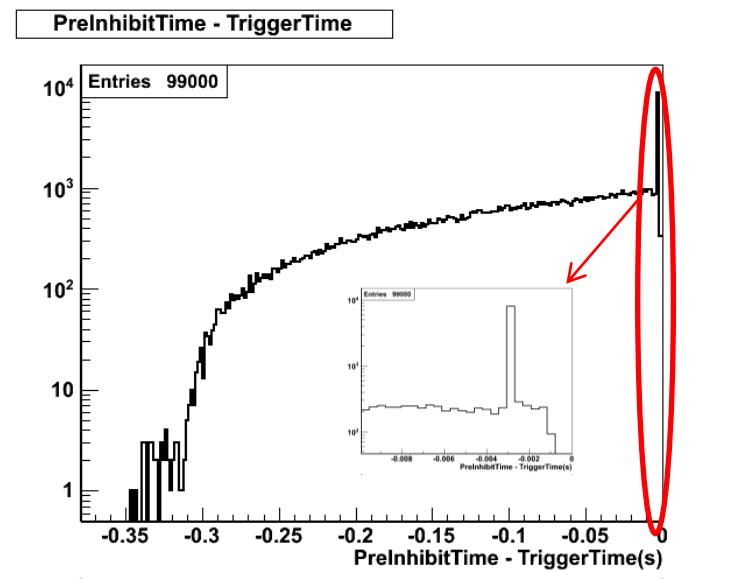
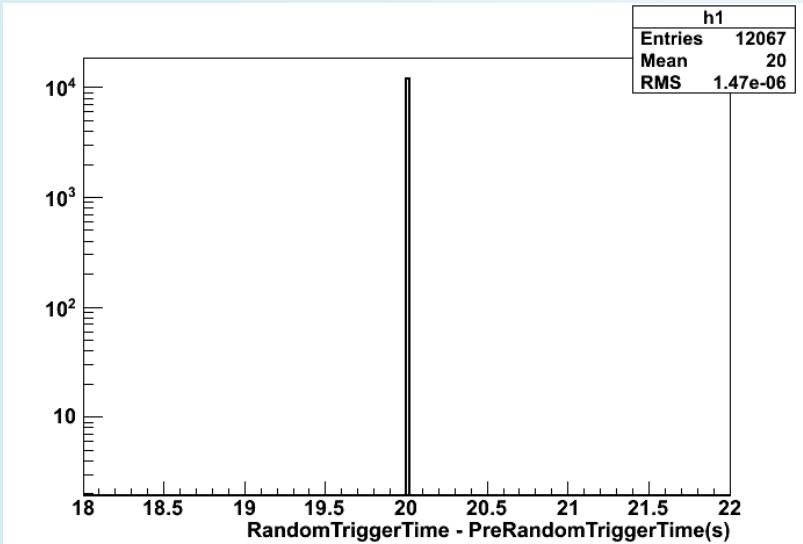
**China Jinping Underground Laboratory**

# Time recording

	<b>Module type</b>	<b>Time precision</b>	<b>Need system trigger?</b>	<b>Recording time range</b>
Computer	Computer	~ms	Yes	System trigger time
FADC	V1724	20ns	Yes	System trigger time
TDC	V1290N	25ps	Yes	Hits between 51μs before trigger time and 1μs after trigger time
ETTT	V1290N	800ns(25ps)	No	Full time scale range

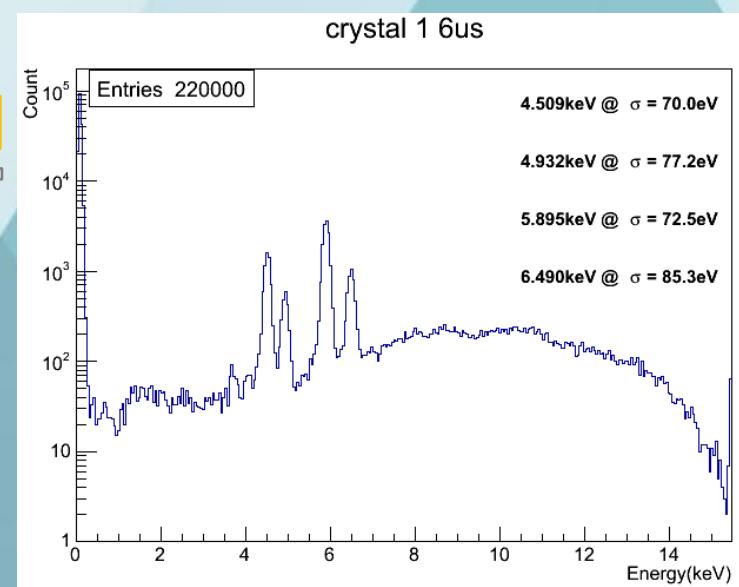
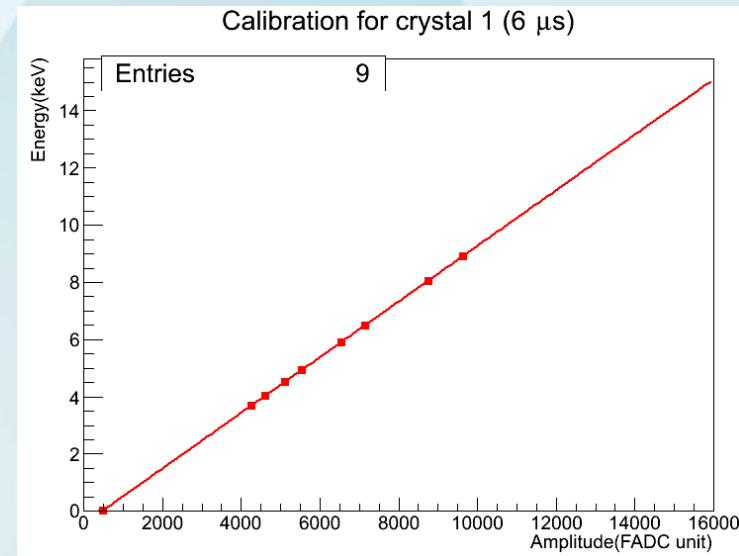
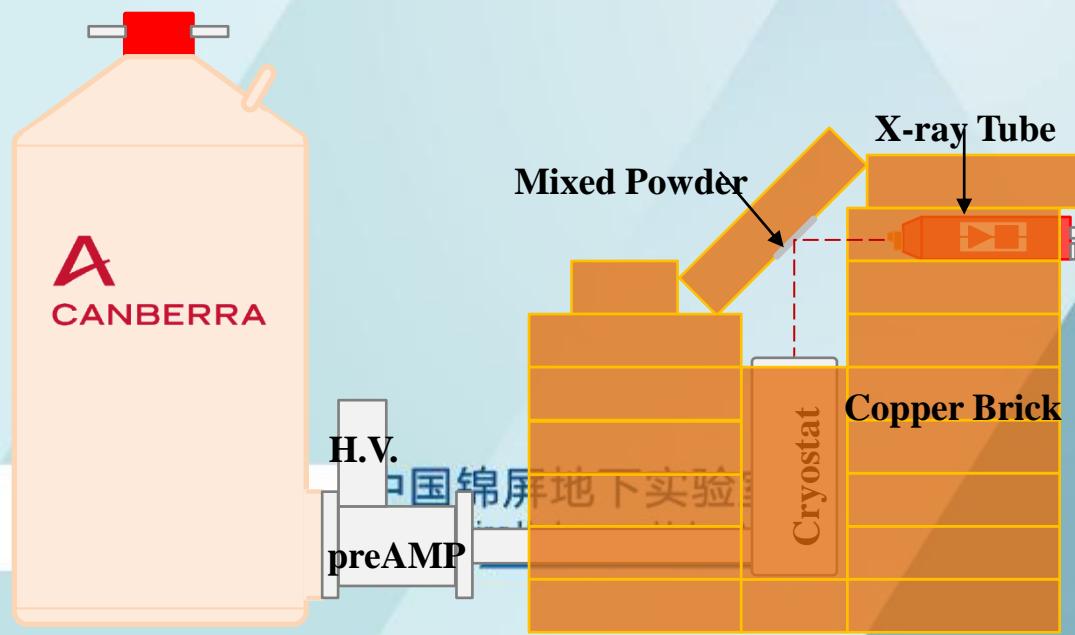
# The DAQ test results for 20g ULEG & AC

- The test results for time recording



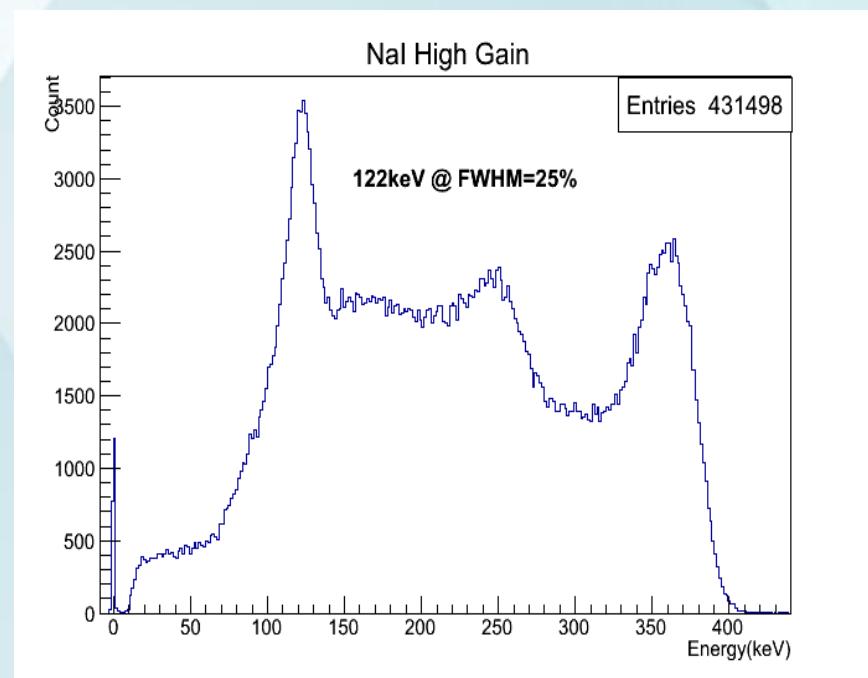
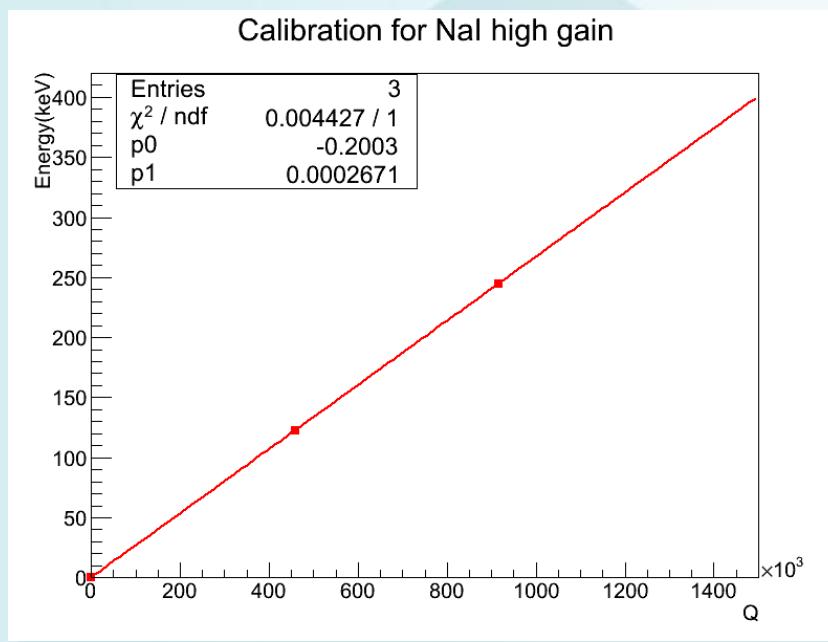
# Detector Calibration

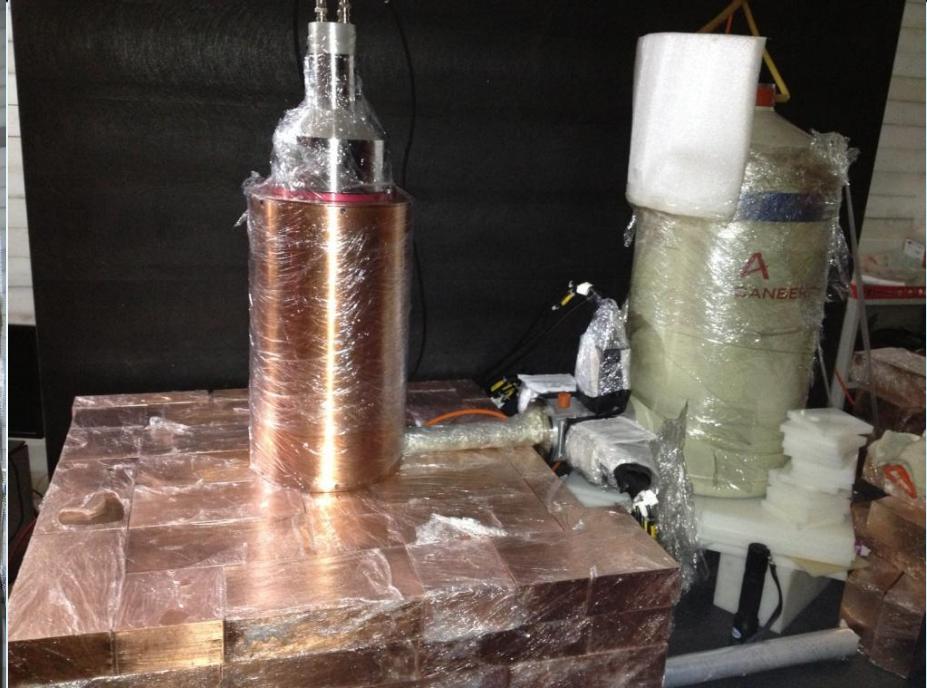
	E/keV
RT	0
Ca	3.69
	4.01
Ti	4.51
	4.93
Mn	5.90
	6.49
Cu	8.05
	8.90



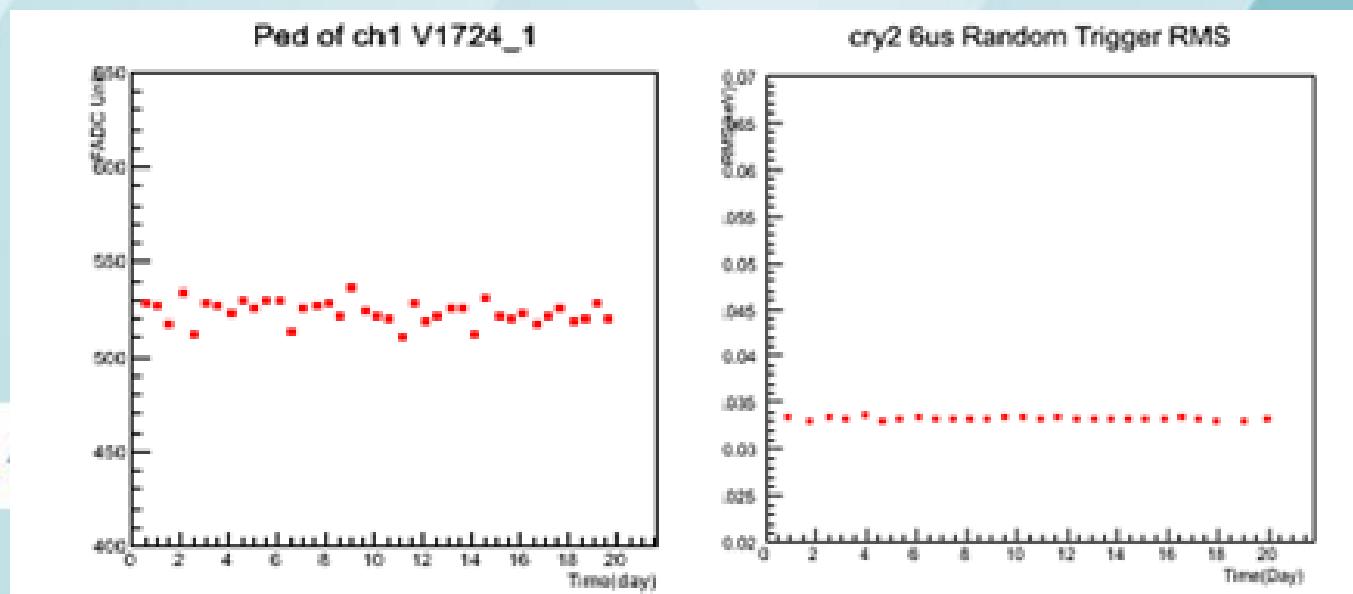
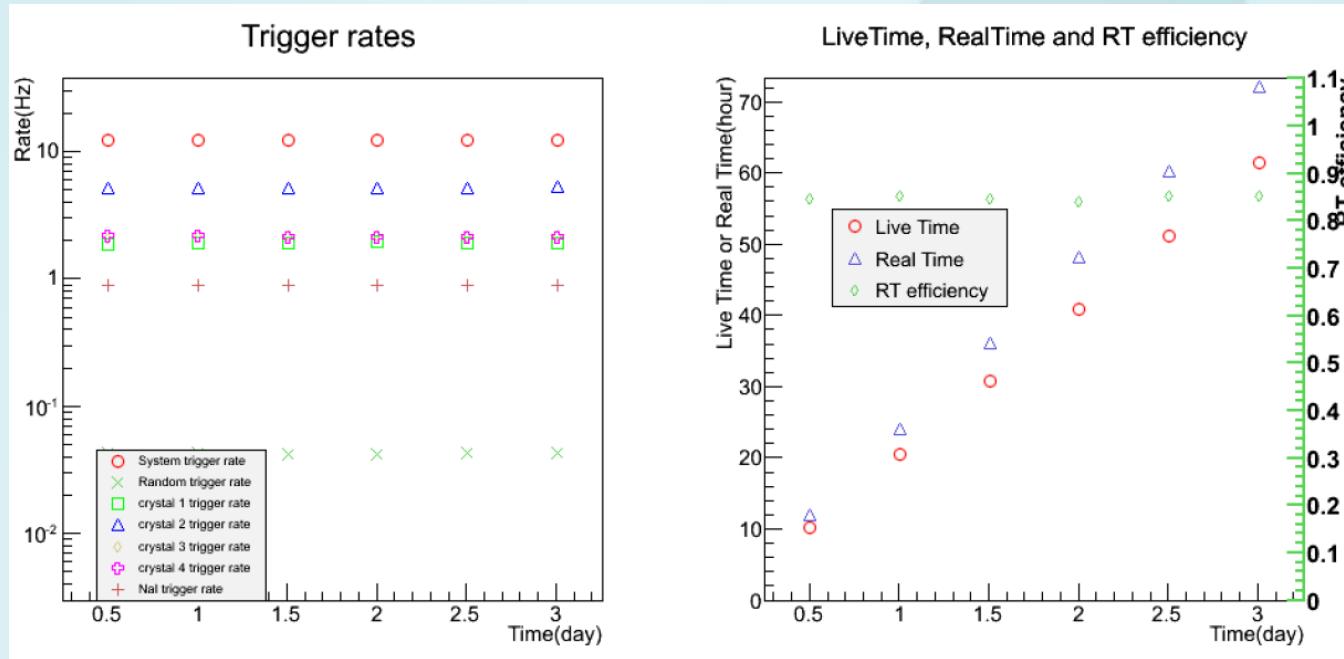
# NaI(Tl) Detector Calibration

$^{152}\text{Eu}$  sample





# DAQ and data monitor



# Summary & outlook

- The new DAQ system for Ge & AC has been set up
- Set up the DAQ system for 1kg PPCGe & anti-Compton detector.
- Upgrade the DAQ system (FPGA)



中国锦屏地下实验室  
China Jinping Underground Laboratory

# Thanks for your attention!



中国锦屏地下实验室  
China Jinping Underground Laboratory