Minutes, 67th PXD EVO Meeting, 11.12.2013 14:00

Present: H.-G. Moser, A. Campbell, C. Marinas, C. Kiesling, D. Levit, S. Lange, S. Tanaka, C. Niebuhr, T. Ferber, D. Esperante, C. Lacasta, E. Lüttike, M. Ritzert, I. Konorov, M. Lemarenko, S. Rummel, F. Müller, B. Lobotsinski, M. Valentan, Z. Dolezal, A. Frey

Agenda

* Wednesday, 11 December 2013
	+ 14:00 - 14:20DHPT 1.0 Tests *20'*

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| Speaker: | Mikhail Lemarenko |
| Material: | [**Slides**](https://indico.mpp.mpg.de/materialDisplay.py?contribId=3&materialId=slides&confId=2626)pdf file |

* + 14:20 - 14:40DAQ Status *20'*

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| Speaker: | Sören Lange |
| Material: | [**Slides**](https://indico.mpp.mpg.de/materialDisplay.py?contribId=0&materialId=slides&confId=2626)pdf file |

* + 14:40 - 15:00Beam Test Status *20'*

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| Speaker: | Carlos Marinas |
| Material: | [**Slides**](https://indico.mpp.mpg.de/materialDisplay.py?contribId=2&materialId=slides&confId=2626)pdf file |

* + 15:00 - 15:20Slow Control Status *20'*

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| Speaker: | Michael Ritzert |

* + 15:20 - 15:40AOB *20'*

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* + - Conferences and Workshops *15'*

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| Speaker: | Zdenek Dolezal |

* + - DPG *15'*

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**DHPT 1.0 Tests (M. Lemarenko):**

DHPT 1.0 is the new version of the DHP in TSMC 65nm technology. Besides that it has some improvements compared to the IBM version (DHP 0.2), notable more buffer space for higher throughput and support of switcherG (for gated operation).

First test results show that the new DHPT 1.0 works fine, and will probably be usable for EMCM and other upcoming tests. However, some deviations from the default conditions are necessary to run it properly (in order to run the sequencer at full speed the supply voltage needs to be increased). Further tests are needed for complete chip investigation (data processing, DCD-DHP integration). For production, another chip iteration (DHPT 1.1?) will be scheduled to fix the minor bug found. This could happen in March/April 2014. Given the fast turnaround of TSMC this has no impact on the schedule.

**DAQ Status (S. Lange):**

New AMC V3 cards (8) arrived at Giessen and were tested. 5 are ok. Together with 3 old ones and V2 cards there are enough for the beam test and parallel debugging. This week and next week people from Giessen are at DESY to prepare the DAQ.

Giessen is preparing 3 pocket-onsen system (for KEK, beam test and Giessen).

**Beam Test Preparations (Carlos Marinas):**

SwitcherB 1.8G was tested successfully on hybrid 5 with DHP0.2.

ONSEN+DHH is currently at DESY.

A new firmware exists to allow the integration of the full read-out chain with DHH/DHHC.

Next week DATCON-ONSEN-DHHC will be set up at DESY.

MARCO in TB24 is connected to a dummy heat load. Pressure tests are ongoing; interlock logic OK. Installation of the transfer lines is scheduled by today.

 Rail system into PCMAG ready.

**Slow Control Status (M. Ritzert):**

Slow control will be prepared at DESY from January 3-6.

**AOB:**

There are two conferences where we should contribute: INSTR 2014 conference in Novosibirsk in February 24 to March 1, 2014.

Candidates should be nominated soon, since visa arrangement might take some time.

We would also like to find a volunteer for a talk at TIPP in Amsterdam, where the abstract should be submitted by January 31.

Deadline for contributions to the DPG spring meeting is December 15. Abstracts should be sent by to Christian Kiesling by Thursday. He will post them in our Twiki an resolve clashes – if any.