Minutes, 68th PXD EVO Meeting, 16.01.2014, 14:30

Present: H.-G. Moser, C. Marinas, C. Kiesling, S. Tanaka, C. Niebuhr, I. Peric. C. Kreidl, R. Richter, L. Andricek, F. Lüttike, I. Konorov, S. Rummel, F. Müller, Z. Dolezal,… (and more)

Agenda

* Thursday, 16 January 2014
	+ 14:30 - 15:15Beam Test

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| Convener: | Carlos Marinas |

* + 15:15 - 16:00AGENDA SVX Workshop DESY

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| Convener: | Hans-Günther Moser |
| Material: | [**Indico of SVD/PXD agenda**](https://indico.mpp.mpg.de/materialDisplay.py?sessionId=1&materialId=0&confId=2660)link |

**Beam Test Report (Carlos Marinas)**

MARCO is running. It is presently connected to a dummy load. We will not connect it to the PXD, since there is a risk of damage due to the large CTE mismatch of the PCB and the silicon module (which caused already some deformation). In principle it is possible to operate MARCO at 18 deg, for this the water cooling is doing the job as well. So MARCO will only be used for the SVD (cold).

For the PXD bench power supplies are used (in T24 next to the TB area). The LMU power supplies are set up in parallel and tested with a bare hybrid 6. They will be used later.

The PXD module (J00) has been powered up after arrival in Hamburg and checked. It was then installed in the thermal envelop and checked again (pedestals, laser signal could be detected). The pedestal distribution is a bit ugly and the noise is rather high (0.9 ADC) but there was no optimization yet. The clock operates at 250 MHz (full speed would be 320 MHz).

Next objective: readout with ONSEN and take data.

After the PXD installation SVD observed higher noise. PXD was unpowered and power cables disconnected which did not reduce the noise. The SVD crew is still investigating.

The second module (I06) has been sent to Finetech for repair. Finetech says that the can remove the switcher, clean the pads and rebond a new switcher. They will start with adding two switcher. If this works smoothly (x-ray check) they could add the other two. Replacement swithers arrived at HLL today and will be sent to Finetech. After repair Finetech will send the module directly to DESY (Laci Andricek).

In parallel an EMCM module is being prepared. It came back from Finetech with the passives and is now at MPP to add the Kapton. Later it will go back to the HLL and a (small) PXD6 matrix will be mounted. It can be brought to DESY next Wednesday.

In addition the holding frame for a hybrid 5 module (already tested and understood) is prepared (design ready, fabrication either at MPP or DESY).

Priority will be to collect data with J00 alone. Depending on the success of the rework I00 or EMCM or hybrid 5 will be added but only after the PXD/SVD workshop (January 24).

**Agenda of the PXD/SVD workshop (DESY, January 22-24)**

The agenda is on Indico:

<https://indico.desy.de/conferenceDisplay.py?confId=9093>

PXD parallel sessions will be on Thursday afternoon and Friday morning. Thursday there will be a session on sensor production, EMCM and tests organized by Laci Andricek. Ivan Peric will organize the session on ASICs, reporting on the status of the new DCD, DHP and SwitherG. We need to discuss how the next submissions should b scheduled.

Friday morning Sören Lange will organize the DAQ session followed by Stefan Rummel (services and PS).