PXD Cosmic Test



University of Tabuk

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Overview

- Tabuk University is involved in PXD tasks: mainly software development and preparation of cosmic-ray test setup at Tabuk
- The main aim for Tabuk cosmic test is to study PXD Tracking characteristics on long term (efficiency and environmental stability etc) before cosmic test at KEK
- Setup will proceed in two steps:
 - first the readout for the PXD using EMCM (Electronic Multi Chip Module) and DAQ will be setup
 - later the telescope and scintillators will be added for tracking and triggering

Basic layout



System as it is installed at MPP



Slow Control (PSs for now) I



Slow Control (PSs for now) II Status

- HMP4040 PSs fully standalone tested: Driver installed, Serial Ports configured, and finally communication with computer tested.

PSs sit on a Rack near Control PC



Port c			coms -		Reset			
Status OK - all voltages below limit								
Settings					Measurement			
	υM	I [mA]	Limit [V]		U [V]	I [m.A.]	OUT?	
Ch1	10	1	12	Cutput	0.000	D	•	
Ch2	8	1	12	Cutput	0.000	D	•	
Ch3	5	1	12	Cutput	0.000	0	•	
Ch4	3	1	12	Cutput	0.000	D	•	
	Send Val	ues		DHP setti	ngs	DCD settin	0 8	

Standalone test Program

Values set on the PS



The SL program to set PSs Values is here in the right. We need to have all the PSs Including TOE ones to run it (from Felix).



PXD hardware and **DAQ**

- All Hardware is ready at MPP to be shipped
- A PC fully installed with BonnDAQ is also ready for shipment. The software tested by Ben Oberhof while at MPP.
- Expect to arrive in a couple of weeks.
- Chiller will arrive in two weeks.
- ATCA crate by Igor is out of order from Pentair And need to find a replacement. Igor is looking.



Alibava System

- Telescope will be made up from Alibava Systems
- For each system:
 - 1 Mother Board directly connected to PC
 - 1 Detector board connected to each Mother Board
 - Aluminium casing for all boards



- Telescope will consist of 2D 1x1 cm² strip silicon sensors with 80um pitch.
- Every Detector Board can handle 128 channels for each direction (u, v)
- Full telescope will consist of two raws of planes upstream and downstream

Trigger Logic Unit (TLU)

- TLU unit was developed for EUDET telescope, different possible operating modes (simple, trigger-busy handshake, trigger-data handshake)
- TLU would be the ideal SIMPLE solution to trigger PXD and Alibava with ALIBAVA DAQ sending a BUSY signal and clear when done.
- DHH naturally accepts signal triggers via ethernet cable from TLU
- Lemo outputs with NIM standard are available for Alibava System



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Full system sketch



Full system sketch (2)



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Conclusion and Schedule

- Some PSs (HMP4040) received and tested at Tabuk. Also SL software installed. Optionally use just HMP4040 PS (so only USBs) and modify the C# program on windows7.

- Full DAQ with EMCM setup fully configurated and tested at MPP. PC ready. Parts and the PC will shipped soon to Tabuk.

- Three 2D 1x1cm² Alibava trackers planes were ordered. Full Telscope system with many planes will be ordered with the new budget next year.

- Trigger: Manufacturing of scintillators at MPP and we are discussing with Hamamatsu for the right small PMTs.

• Cosmic test is expected to be ready to run in 2015 for, at least, 1 year

- Most crucial now:
 - check for availability of TLU unit, otherwise we will have to think of a plan-B: Now CAEN is looking to build a simular one ...

Backing slides

DAQ software: to-do

- Most of the aforementioned SW is currently used at MPP
- There are still some open issues on software
- Bonn DAQ needs some development

to properly read out full PXD matrix





- We have to implement PS control in EPICS (currently a Win-app on a separate machine)
- Furthermore: need to decide how to merge data from PXD and Alibava for tracking and analysis (basf2?)

PXD Readout

- The PXD is readout via DataPatchPanel by the DHH
- DHH is connected directly and via router to PC and data is aquired via BonnDAQ



• The trigger signal is given to DHH via ethernet

PXD-DAQ sofware overview

CSS interface for Epics: chip parameters control, DHH config JTAG control for DHP read-out

dhh0:dhp0

sw enable

sw clear start

sw clear stop

sw gate start

sw gate stop

chip id

V

9 ÷9

12 ÷ 12

18

14 🕂 14

0 ÷ 0

click to choose ini file

Open ini file in

editor

Core Global

mode

pedestal subtraction

cm correction en

invert input values

send buf data only

dcd use right

IDLE 1 IDLE

1

•

Disable Channel

PS control on Adorea Windows Desktop

Bonn DAQ:

latency 0 ÷0 sw clk start 8 ÷8 last row 31 ÷31 sw frame start 0 ÷0 randgen out fifo 0 ÷0 offset en 1 frame sync proc dly 0 ÷0 offset row dly 0 ÷0	run DHP ini scrint	Run control, Event builder, DQM
row2sync dcd clk dly0 $\stackrel{+}{\rightarrow}$ 0offsetdesdly0 $\stackrel{+}{\rightarrow}$ 0frame sync dcd clk27 $\stackrel{-}{\rightarrow}$ 27randgenon \blacksquare cm offset60 $\stackrel{+}{\rightarrow}$ 60howlogalign400 $\stackrel{+}{\rightarrow}$ 400pedestal offset18 $\stackrel{+}{\rightarrow}$ 18docc \checkmark threshold5 $\stackrel{+}{\rightarrow}$ 5double out bits \blacksquare	run parseMem	Config Ibit Write to File DHP on Baur Floor RCMSE Verif des 2015: 5th 1950 DB 185: 006-185 RCMSE Verif des 2015: 5th 1950 DB 185: 006-185 File Prefix: See Prefix num Num: See Prefix Prefix DB 185: Nees-7 Prefix Bet Prefix Prefix DB 185: Nees-7 Prefix DB 185: Nees-7 Pref
write to JTAG read JTAG Read full frame	Row	Hist PLOT Hist RESET Hist EVB Hist RECV Hist SEND Hist S
Number of frames: 0 $\frac{1}{\sqrt{2}}$ Batch read	Execute Python	Command: Server: Jocalic Command:
DHH dhh_receiver: DHH read out DEPFET Pisa		Consected to Localizest 13787
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Preliminary Setup

• In order to test PXD-DAQ and software we will EMCM as test setup



- DHH will be connected to the EMCM to test DAQ, PS, cooling and SW
- Almost the same setup as for cosmic test but without telescope+scintillators and trigger unit (TLU)
- Basic components: EMCM, DataPatchPanel, DHH, PS, Cooling and PC



Study of Trigger and the Alibava Trackers

- We are continually in contact with Carlos and Carmen at Alibava systems
- Now available and can be produced 2x2cm² 1D silicon strips planes and 1x1cm² 2D silicon planes. We have chosen 2D 1x1cm² Planes to avoid systematics from mounting many 1D planes. 2D planes will also make things easier. Available PXD6 modules (up to 4.6cmx0.96cm).
- The 2D planes has x and y strips layers 300 microns, 80 micron pitch, width and separated with a 1.6 mm substrate gap between x and y strips planes.
- Need to define scintillators that maximize data taking rate.
- A simulation task started to find best design of trigger and tracker system.

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Residuals show bumps due to tilted Muons. 1D planes separation will add such effect so 2D planes is better. Residuals will be studied more.



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