

6th Belle II PXD/SVD workshop and 17th International Workshop on DEPFET Detectors and Applications

Thursday, 2 October 2014

Parallel: SVD - Room 131 (09:00 - 11:00)

time	[id] title	presenter
09:00	[19] Indroduction and Topics	Mr IRMLER, Christian
09:10	[20] SVD Mechanics Update and Schedule	BUCHSTEINER, Florian
09:35	[21] SVD Electronics and Firmware Status and Schedule	THALMEIER, Richard
10:00	[24] SVD Power Supplies	FORTI, Francesco
10:15	[22] Electrical Tests of Modules and Ladders	Dr FRIEDL, Markus
10:40	[23] SVD Software	BOZEK, Andrzej

Parallel: SVD - Room 131 (11:30 - 13:00)

time	[id] title	presenter
11:30	[25] QC/QA Procedures Preparation for BPAC	FORTI, Francesco HIGUCHI, Takeo
11:55	[26] Ladder Assembly Procedure (final)	Dr ONUKI, Yoshiyuki
12:20	[27] Ladder Mount Procedure	Dr TSUBOYAMA, Toru
12:45	[28] contingency/Discussion	

Parallel: SVD - Room 131 (14:00 - 15:30)

time	[id] title	presenter
14:00	[29] Ladder Parts Production Status, Issues and Schedule	Dr HARA, Koji
14:30	[30] BPAC Preparation and SVD Schedule	Mr IRMLER, Christian
15:00	[31] Logistics 1 - Shipment of Components to/Between Assembly Sites (IPR)	Dr FRIEDL, Markus BETTARINI, Stefano
15:15	[32] Logistics 2 - Final Shipment of Assemblies/Ladder to Japan	Dr HARA, Koji Dr TSUBOYAMA, Toru

Parallel: SVD - Room 131 (16:00 - 17:30)

time	[id] title	presenter
16:00	[63] FOS for SVD	VITALE, Lorenzo
16:15	[33] Construction DB Tutorial	WÜRKNER, Benedikt Dr BERGAUER, Thomas
17:15	[34] Contingency/Discussion	

Parallel: SVD IB - Room 131 (17:30 - 19:00)