Coating entrance window

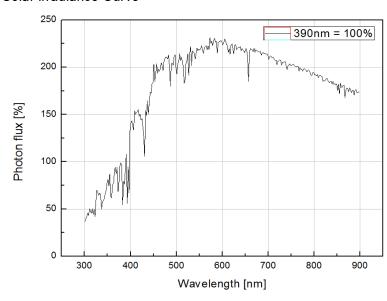


Requirements:

- > 0.6 across the whole spectral working range
- > 0.9 between 380 nm and 500 nm

Maximum @ 390nm

Solar irradiance Curve



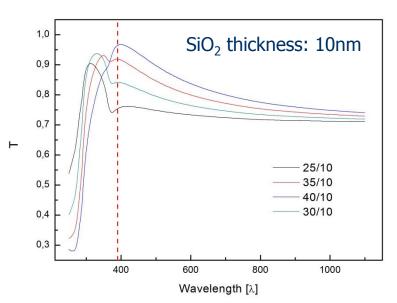
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Coating entrance window Variation SiN thickness

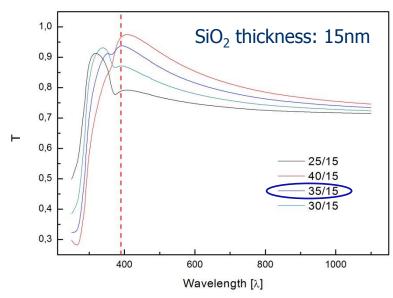




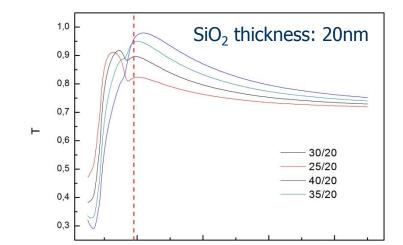
Entrance window: SiN [nm] / SiO2 [nm]



Entrance window: SiN [nm] / SiO2 [nm]



Entrance window: SiN [nm] / SiO2 [nm]



Coating entrance window



Gigahertz-Optik measurements

- Next calibration weeks 38&39 (15.9. 26.9.).
- Diodes from CCD17 with ~30/10 will be measured.

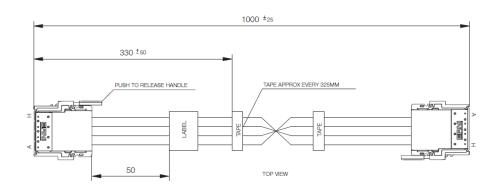
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Status camera electronic



- Finalisation of Sequencer card specification from ProDesign is expected at the end of this week.
- First prototype should be available in September/October 2014.
- Sequencer ERmet ZD cabel (10Gbit/s) with 2m till October
 → Is 2m enough?





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