

# Summary of Mainz Slow Control Meeting 30./31.07.2014 @ Mainz







Sören Lange (Giessen)  
SC Meeting, MPI München, 23./24.09.2014





Bundesministerium  
für Bildung  
und Forschung

# Ahttps://indico.uni-giessen.de/indico/conferenceDisplay.py?confId=169

Wednesday, 30 July 2014

- 11:15 - 11:30 Participants: C. Sfienti, M. Hoek, M. Thiel (Mainz), C. Kiesling, M. Valentan (MPI), T. Röder (TUM), M. Ritzert (Heidelberg), B. Spruck, T. Gessler, D. Getzkow, S. Lange, K. Lautenbach, M. Wagner (Giessen)
- 11:30 - 11:45 Welcome 15'  
Speaker: Prof. Concettina Sfienti (Institut für Kernphysik - Johannes Gutenberg-Universität Mainz)
- 11:45 - 12:15 Introduction to PXD 30'  
Speaker: Prof. Christian Kiesling (Max-Planck-Institute for Physics)  
Material: [Slides](#) 
- 12:15 - 12:30 Introduction to PXD DAQ 15'  
Speaker: Soeren Lange (JLU -Giessen)  
Material: [Slides](#) 
- 12:30 - 13:00 Questions and discussion 30'
- 13:00 - 14:00 LUNCH
- 14:00 - 14:30 IPMI @ Giessen (boards, microcontroller, ipmitools) 30'  
Speaker: Thomas Geßler (Univ. Gießen)  
Material: [Slides](#) 
- 14:30 - 15:00 DQM (basf2 DQM, DQM during DESY test) 30'  
Speaker: Björn Spruck (II Physik Uni Gießen)  
Material: [Slides](#) 
- 15:00 - 16:00 Serviette: what data quality monitoring is where ? 1h0'  
Material: [Slides](#) 
- 16:00 - 17:00 Discussion: Physics Analysis Topics 1h0'
- 17:00 - 18:00 Blackboard: what slow control is where ? 1h0'  
Material: [Slides](#) 
- 18:00 - 20:00 DINNER

Thursday, 31 July 2014

- 10:15 - 12:30 EPICS @ Belle II (Introduction & Tutorial) 2h15'  
Speaker: Michael Ritzert (Heidelberg)  
Material: [Slides](#) 
- 12:30 - 13:00 EPICS @ Giessen (Onsen System) 30'  
Speaker: Klemens Lautenbach (Giessen)  
Material: [Slides](#) 
- 13:00 - 14:00 LUNCH
- 14:00 - 15:00 Contingency 1h0'

## Summary of Mainz workshop (1/2):

### IPMI for ATCA

- hardware project (small PCB)
- microcontroller programming (ATMEL)
- can be done within a diploma thesis
- to be integrated into EPICS (via ATCA shelf manager)  
example application:  
reboot of a compute node or whole ATCA crate
- synergy with Panda

important for discussion today:

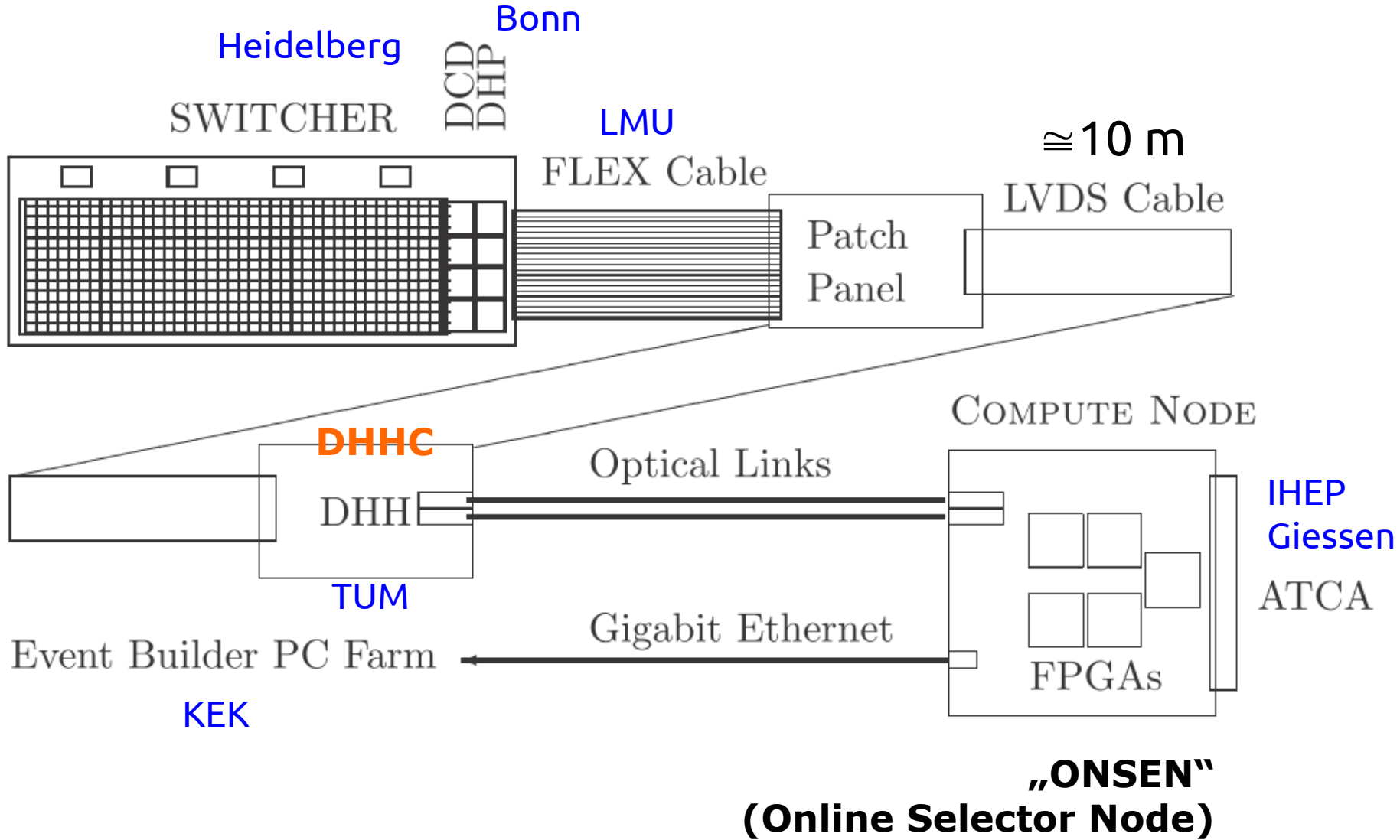
- also needed for DATCON
- also needed for DHH (?)

## Summary of Mainz workshop (2/2):

### QA / Monitoring

- monitoring of compute nodes
  - e.g. data reduction factor
  - FPGA writes into a register
  - powerpc (embedded on FPGA, runs linux) reads the register
  - EPICS connects to powerpc and performs monitoring
  - important for discussion today:
    - monitoring of DHH?
    - monitoring of DATCON?
- monitoring on basf2 (QA histograms)
  - module written by Klemens and Björn (official, in svn)
  - e.g. unpacker detects data errors
  - ("double header", checksum, HLT timeout etc.)

# PXD Readout Chain



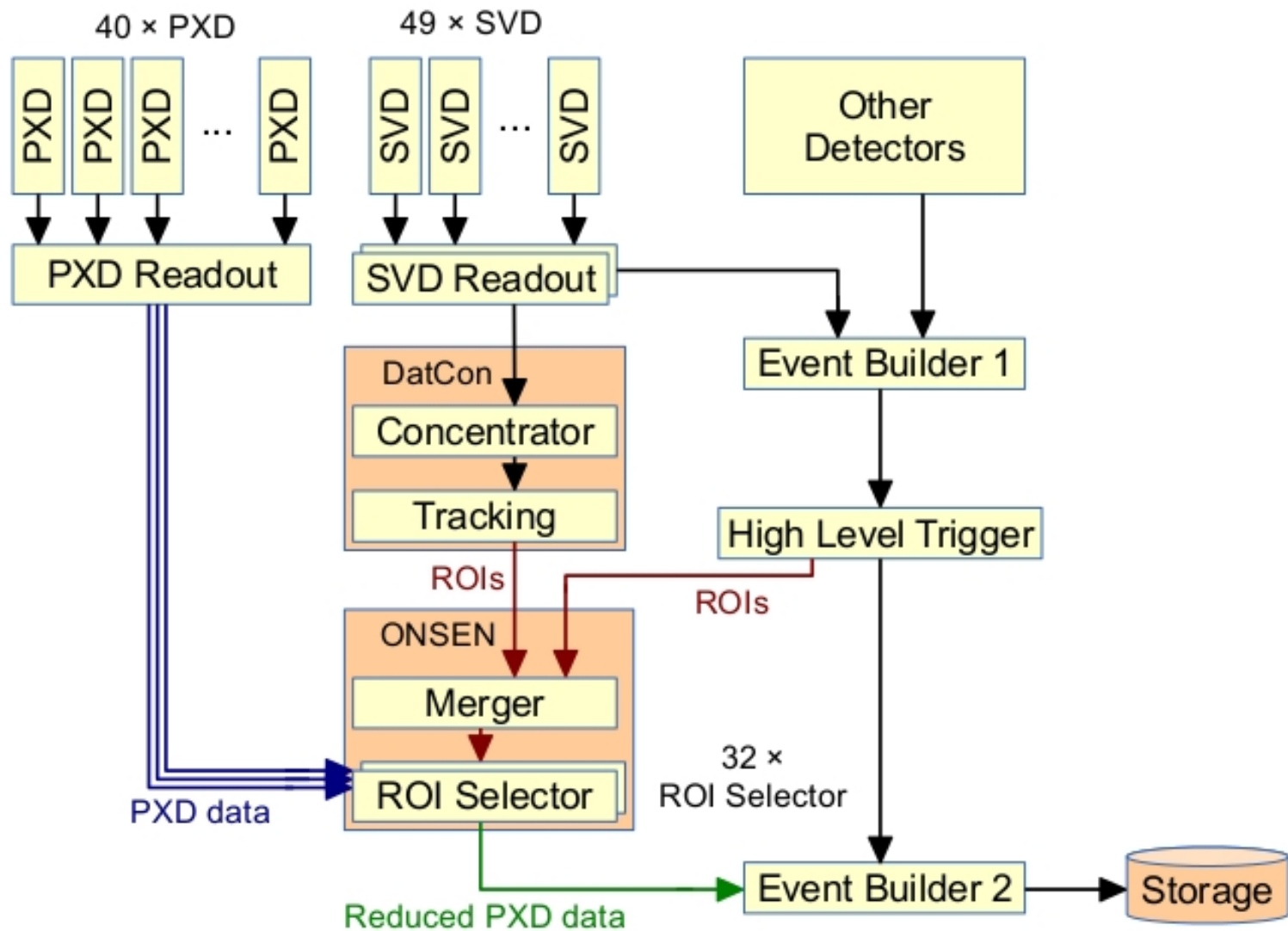


Diagram of the Belle II VXD DAQ for setup at KEK