

7th Belle II VXD Workshop and 18th International Workshop on DEPFET Detectors and Applications

Thursday, 22 January 2015

SVD Parallel Session V: Software - Seminar room T9 (17:00 - 18:40)

-Conveners: Andrzej Bozek

time	[id] title	presenter
17:00	[69] BG simulation development	KVASNICKA, Peter
17:10	[70] BG simulation	DASH, Nibedita
17:20	[71] Software-hardware mapping	BASITHKALIYAR, Abdul
17:30	[72] Raw data packer and unpacker in MC	WIECHCZYNSKI, Jarek
17:40	[73] Clustering and digitalisation in simulation	KVASNICKA, Peter
17:50	[74] SVD geometry implementation	STYPULA, Jacek
18:00	[75] Track finding - SVD issues	JAKOB, Lettenbichler
18:10	[76] Alignment, alignmemt check	BILKA, Tadeas
18:20	[77] SVD Slow Control	YIN, Hao BACHER, Szymon
18:35	[78] SVD software	BOZEK, Andrzej