

- Hybrid 6.0
  - 2 Boards (DCDBv2, SwB1.8G, DHP0.2)
  - Timing investigations, trigger delay
  - DHH debug
  - 2b DAC operation
  - Laser scans/test beams
- Hybrid 5.0
  - 2 Boards (DCDBPipeline, SwB1.8G, DHPT1.0)
  - DCD ↔ DHP communication
  - Irradiations (Sensor and ASICs)
  - DCD optimization. Noise, pedestals
  - Gated mode

- We would like to have 2 working modules in Bonn
  - One on a Hybrid-like assembly
  - One with new kapton design
- Sensor
  - Clear performance
  - Charge collection homogeneity
  - Pedestal distribution
  - Irradiation
- ASICs/System
  - Data transmission/link stability
  - Gated mode
  - Common mode circuitry, 2b DAC, noise, gain, ...
  
  - Missing: LMU PS (arrived yesterday)
  - Deliver 30 tested DHPT1.0