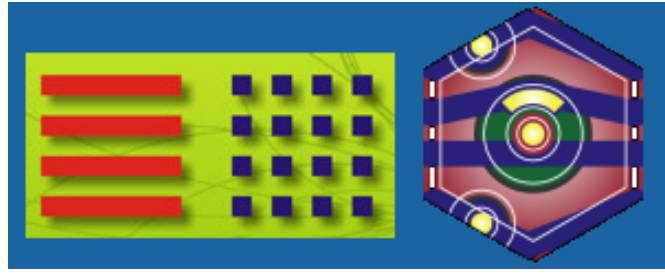


8th Belle II VXD Workshop



Contribution ID: 36

Type: **not specified**

ASIC Boundary Scan

Thursday, 10 September 2015 14:40 (20 minutes)

Presenter: LEITL, Philipp (MPI Munich)

Session Classification: PXD Parallel III