

8th Belle II VXD Workshop

Thursday, 10 September 2015

PXD Parallel III (14:00 - 16:00)

time	[id] title	presenter
14:00	[34] DCD pp: ADC Curves and Simulation	Haidl, Jakob
14:20	[35] Hybrid 5 Testing	Paschen, Botho
14:40	[36] ASIC Boundary Scan	Leitl, Philipp
15:00	[71] Probe Card Status	Mr MARCA, Boronat
15:20	[38] PXD Slow Control	Röder, Thorsten
15:30	[39] PXD Test Beam Plans October 2015	Marinas, Carlos
15:45	[40] X0 Measurements	Stolzenberg, Ulf