

# **Belle II PXD ASIC Review II**

## **Report of Contributions**

Contribution ID: 0

Type: **not specified**

## **Introduction, Schedule, Overview**

*Wednesday, 15 July 2015 10:00 (20 minutes)*

**Presenter:** ANDRICEK, Laci (MPG Halbleiterlabor)

Contribution ID: 1

Type: **not specified**

## **DEPFET Drain Current Dispersion - What to expect**

*Wednesday, 15 July 2015 10:20 (20 minutes)*

**Presenter:** RICHTER, Rainer (MPG HLL)

Contribution ID: 2

Type: **not specified**

## **DEPFET Drain Current Dispersion - impact on Dynamic Range**

*Wednesday, 15 July 2015 10:40 (20 minutes)*

**Presenter:** LUETTICKE, Florian (Uni Bonn)

Contribution ID: 3

Type: **not specified**

## **Summary of the recent X-Ray irradiation campaign**

*Wednesday, 15 July 2015 11:20 (20 minutes)*

**Presenter:** SCHWENKER, Benjamin (University of Göttingen)

Contribution ID: 4

Type: **not specified**

## **Gated Mode seen from the DCD/DHP and SWB perspective**

*Wednesday, 15 July 2015 11:40 (20 minutes)*

**Presenter:** KOFFMANE, Christian (MPI Halbleiterlabor)

Contribution ID: 5

Type: **not specified**

## **DCD-DHPT Interface & DHPT Re-design Status**

*Wednesday, 15 July 2015 13:00 (1 hour)*

**Presenter:** KRUEGER, Hans (Uni Bonn)

Contribution ID: 7

Type: **not specified**

## **DCD - Status and Plans**

*Wednesday, 15 July 2015 14:20 (45 minutes)*

**Presenter:** PERIC, Ivan (KIT)



Contribution ID: 8

Type: **not specified**

## SwitcherB - Status and Plans

*Wednesday, 15 July 2015 15:05 (45 minutes)*

**Presenter:** PERIC, Ivan (KIT)

Contribution ID: 9

Type: **not specified**

## **Closed Session**

*Wednesday, 15 July 2015 15:50 (45 minutes)*