



PXD Slow-Control Task Sharing



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9th VXD Belle II Workshop

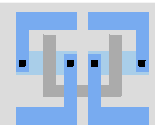
Valencia

14.01.2016

Introduction

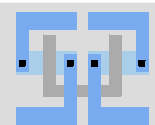
- Granted funding for SC related work way below request.
- ⇒ shortage of manpower
- Obviously, The work load is not reduced...

- So far the approach to develop IOCs together with the hardware has worked remarkably well. If possible, we should stick to it.
- „Responsible“ institute means not the one doing **all** the work, but the one overseeing the development.



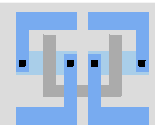
EPICS Drivers (IOCs)

Task	Responsible Institute	Remarks
DATCON		
DHE, ASICs (JTAG)		
FOS		
IBBelle		
IPMI		
ONSEN		
PS		
Belle II (RC/PSC)		
BLM, HW Interlock		



Others

Task	Responsible Institute	Remarks
IPMI Hardware		
DQM Integration		
GUI Integration		
Remote Control Room		



Thank you!