



PXD Powerup-Sequence vs. Belle II RC/PSC



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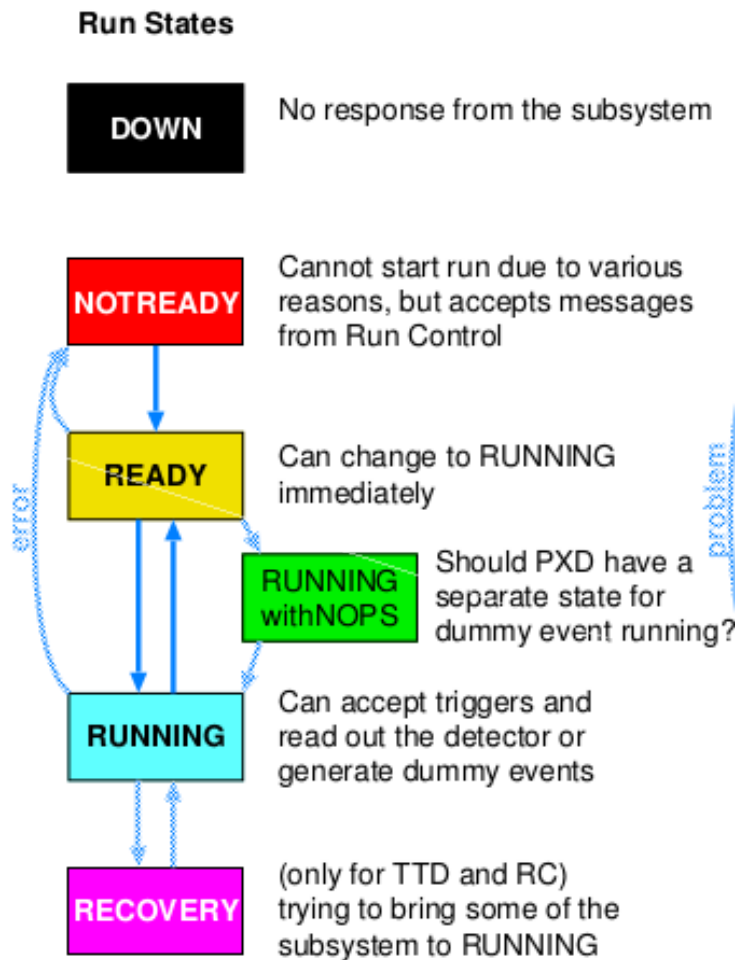
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23rd B2GM

Tsukuba

03.02.2016

Analysis: Run States



Obvious...

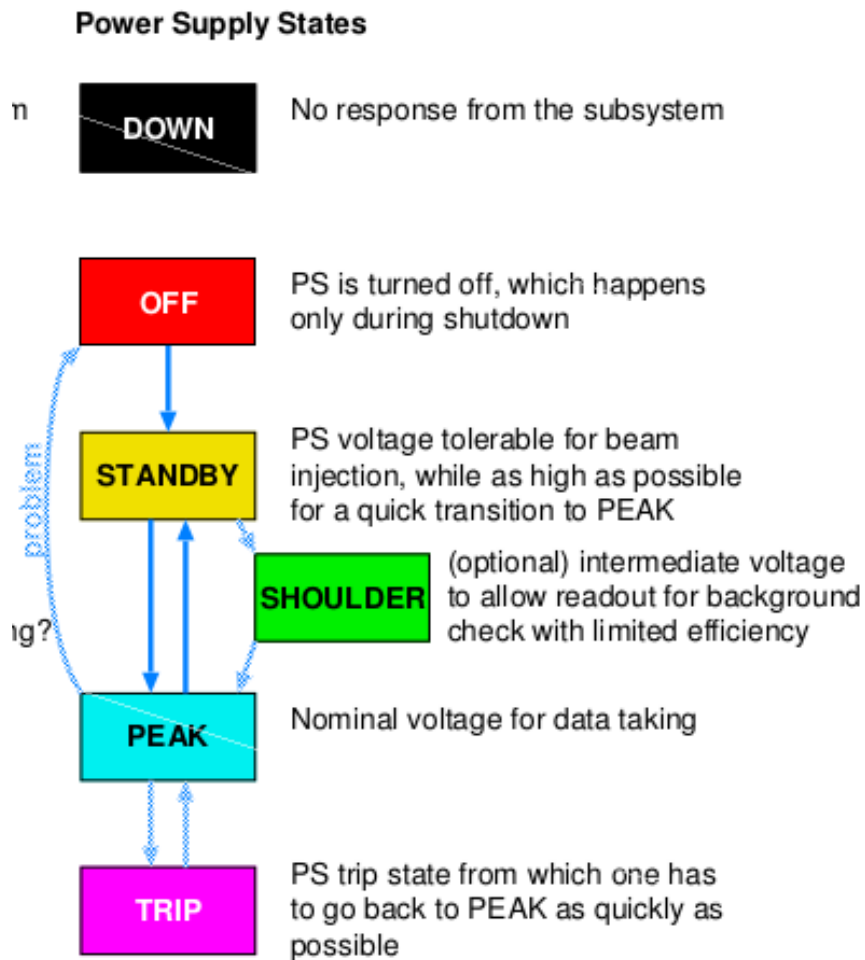
IOC booted

Everything prepared,
triggers ignored

Triggers processed

⇒ All concerning the DAQ part.

Analysis: Power Supply States



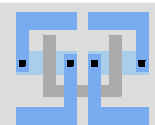
Obvious...

IOC booted
DHH configured

ASICs configured,
DEPFET not powered

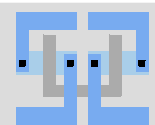
DEPFETs powered

⇒ All concerning the sensor part.



Proposed Mapping

	OFF	STANDBY	PEAK
NOTREADY	DHH configured DAQ off	ASICs+DHH ready DEPFETs not powered DAQ off	Frontend sending data DAQ off
READY	DHH configured DAQ links established	ASICs+DHH ready DEPFETs not powered DAQ links established	Frontend sending data DAQ links established
RUNNING	DHH configured DAQ processing triggers, ROIs.	ASICs+DHH ready DEPFETs not powered DAQ processing triggers, ROIs.	Frontend sending data. DAQ processing triggers, ROIs.



Dummy Mode

	OFF	STANDBY	PEAK
NOTREADY	DHH configured DAQ off	ASICs+DHH ready DEPFETs not powered DAQ off	Frontend sending data DAQ off
READY	DHH configured DAQ links established	ASICs+DHH ready DEPFETs not powered DAQ links established	Frontend sending data DAQ links established
RUNNING	DHH configured DAQ processing triggers, ROIs. DHH sends dummy data	ASICs+DHH ready DEPFETs not powered DAQ processing triggers, ROIs. DHP sends dummy data	Frontend sending data. DAQ processing triggers, ROIs.

