

TTD at DESY-TB2016

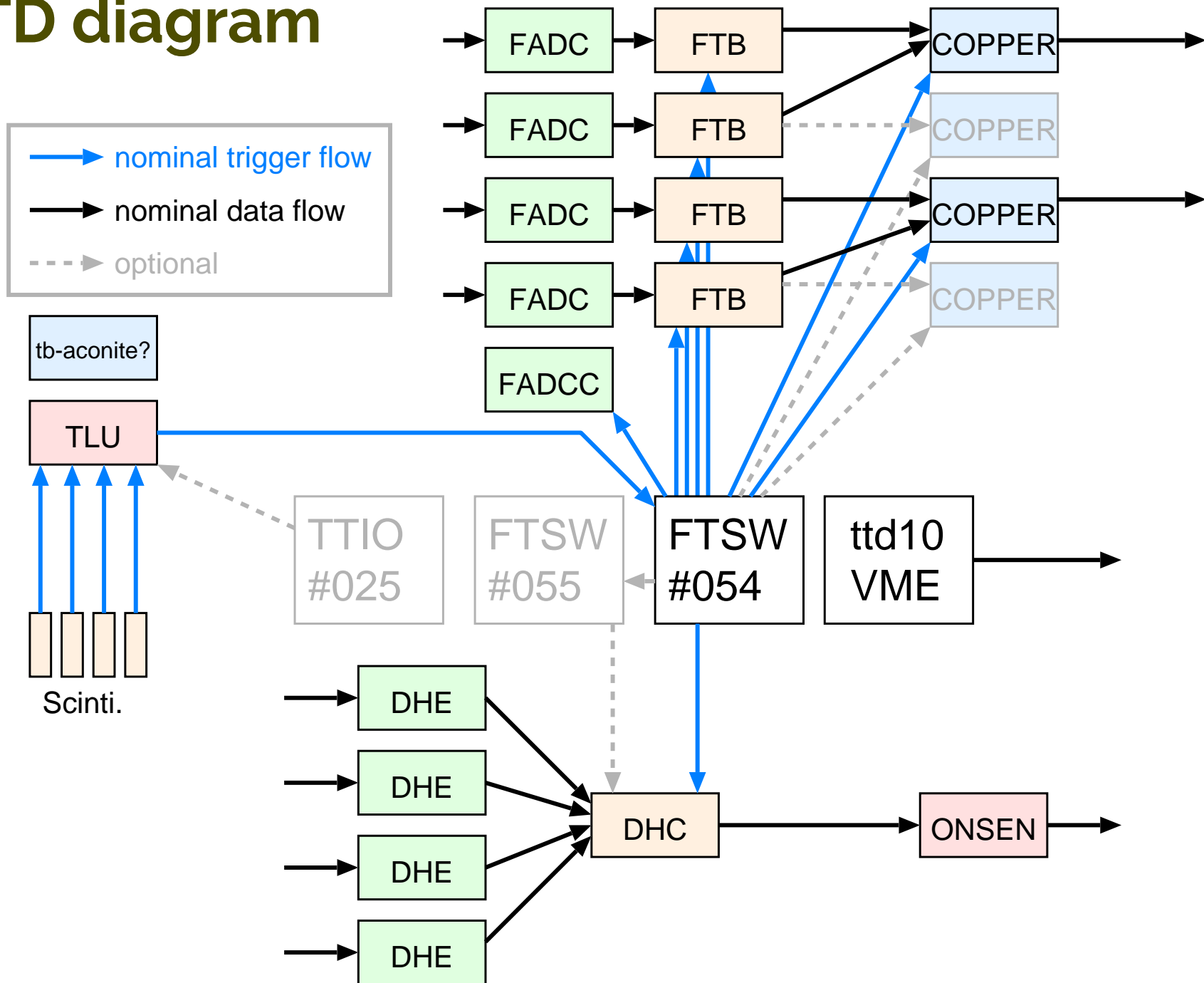
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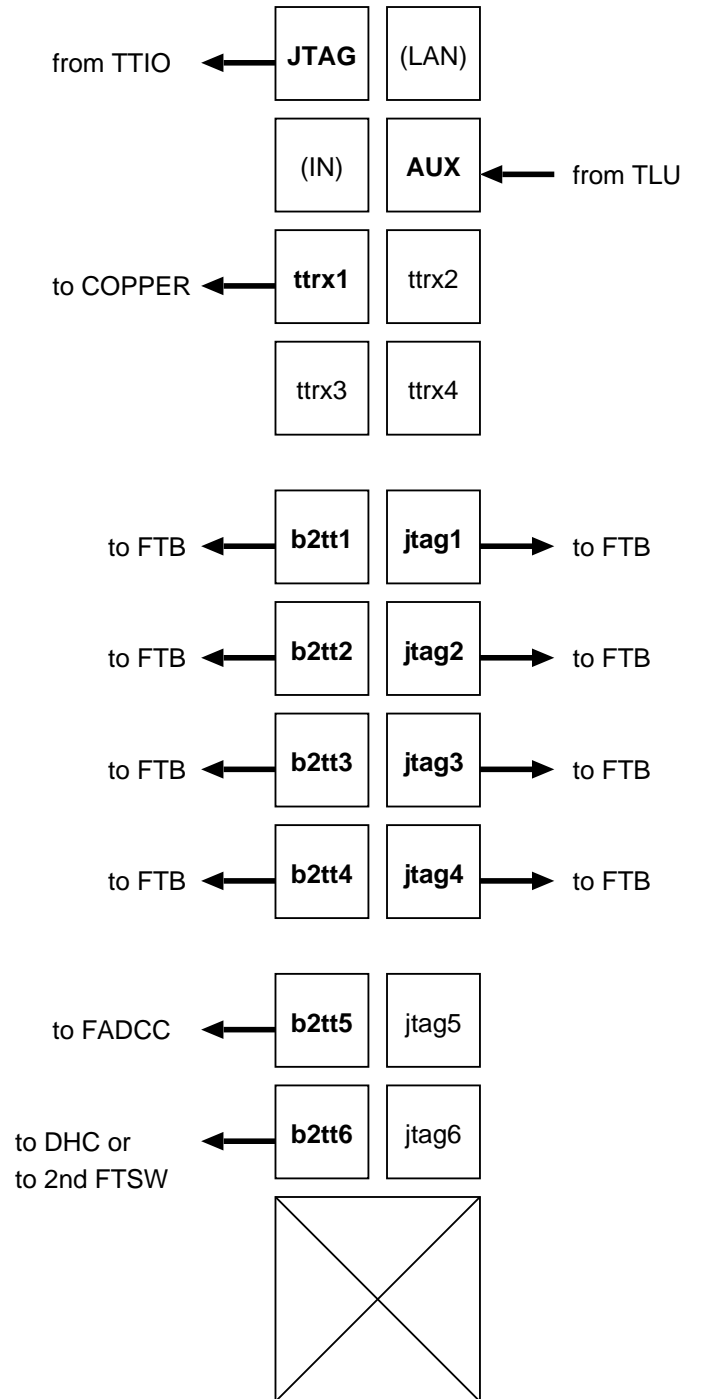
TB-meeting

TTD diagram



FTSW diagram

- Almost all ports are occupied
- No 5th FTB or 5th COPPER without 2nd FTSW
- DHP thru 2nd FTSW may allow independent SVD / PXD operation



Test items

- **TLU connection check since FTSW firmware has been updated**
 - Need half a day at the beginning
 - Is tb-aconite (or something alike) still there? Can I have access from KEK?
- **Integration items**
 - High rate / short trigger interval?
 - Buffer flushing at run change?
 - FADCC deadtime generator?
- **Error detection / back pressure**
 - back pressure from ONSSEN
 - error propagation from DATCON?

(maybe more items to test...)