

10th VXD Belle II Workshop

Wednesday, 14 September 2016

PXD Parallel Session II: PXD9 Module Production and Test (II) (11:00 - 12:00)

-Conveners: Laci Andricek

time	[id] title	presenter
11:00	[45] Gated mode on H5	MARINAS, Carlos
11:20	[43] PXD9 Gated Mode	Dr PRINKER, Eduard
11:40	[44] PXD9 module testing preparations	MÜLLER, Felix