



# 10th VXD Belle II Workshop

## Wednesday, 14 September 2016

### PXD Parallel Session III: ASICs (Measurements) (12:00 - 13:00)

-Conveners: Richard Leys; Ivan Peric

time	[id] title	presenter
12:00	[10] DCDB4.x Analog Performance	Mr WIEDUWILT, Philipp
12:20	[11] DCDB4.x Digital Performance	Mr SCHREECK, Harrison
12:40	[12] DHPT1.2 Measurements	KRÜGER, Hans