10th VXD Belle II Workshop

Wednesday, 14 September 2016

SVD Parallel Session III **(14:30 - 16:00)**

time	[id] title	presenter
14:30	[58] FADC hardware/firmware status	THALMEIER, Richard
14:50	[59] CAEN PS interface, patch pannel requirements	FORTI, Francesco
15:05	[60] SVD DAQ status	Mr NAKAMURA, Katsuro
15:20	[61] Discussion on SVD DAQ integration (system layout, responsabilites, schedule)	