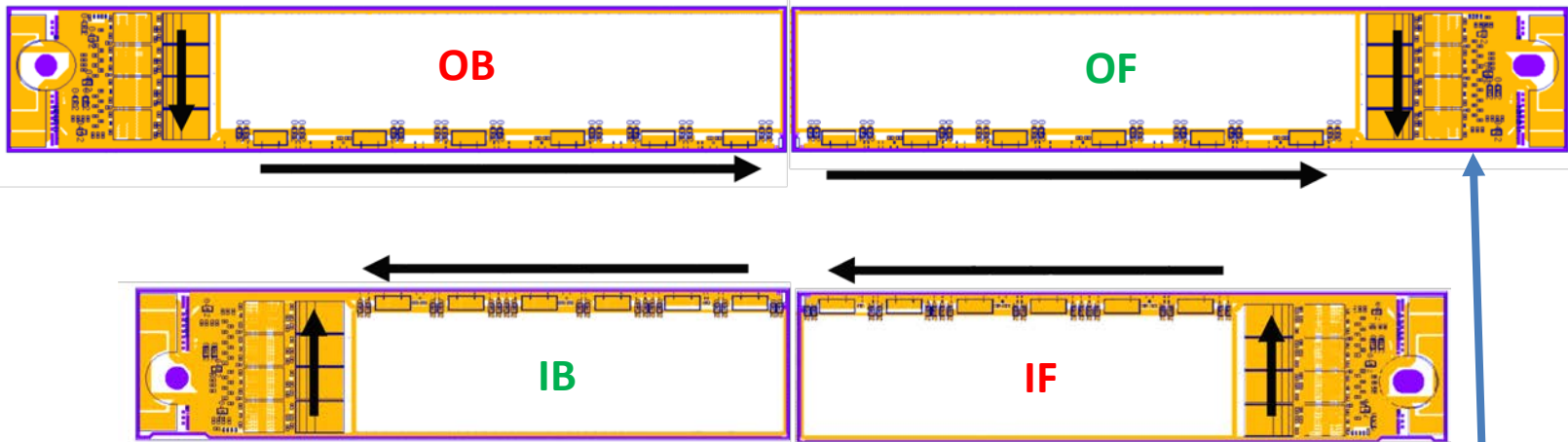


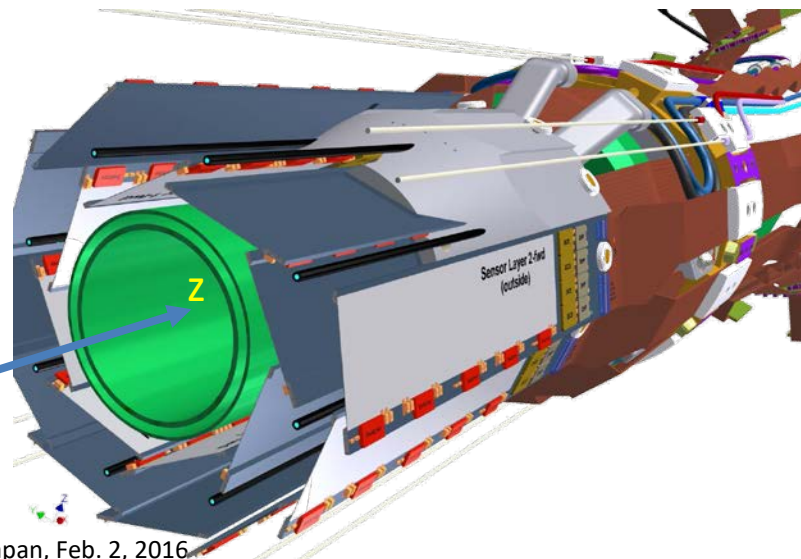
View from “outside” = against beam direction



View from “inside” = in beam direction

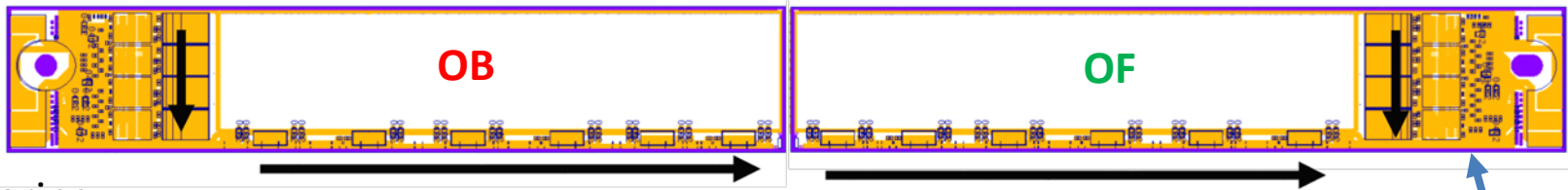
numbering
direction
(= readout
directions”)

x
y
z

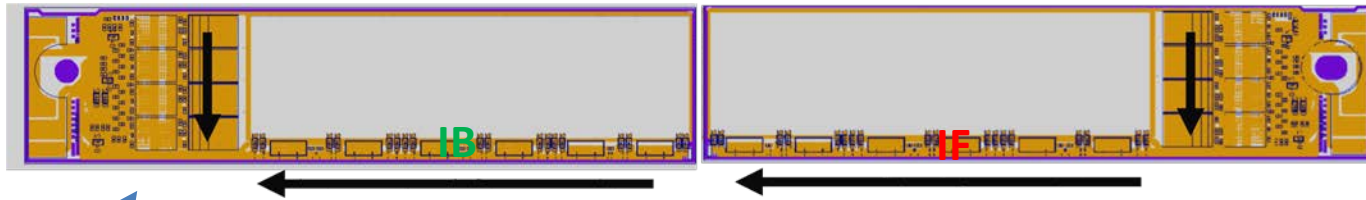


ASICs are “on top”
(looking on the
DEPFET side)

View from “outside” = against beam direction

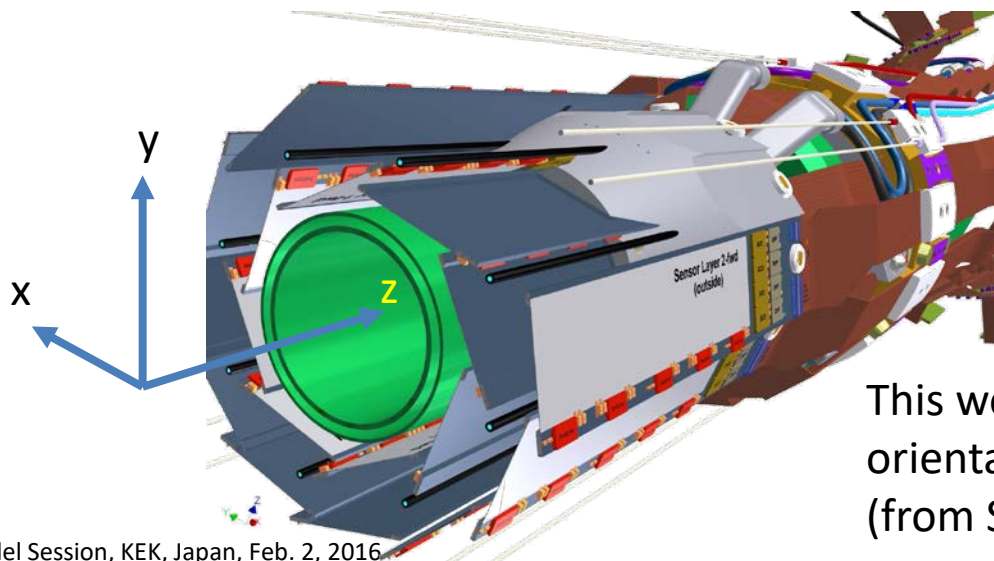


numbering
direction
(= readout
directions”)



View from “outside” = against beam direction

ASICs are
“on bottom”
(looking on the
thinned side)

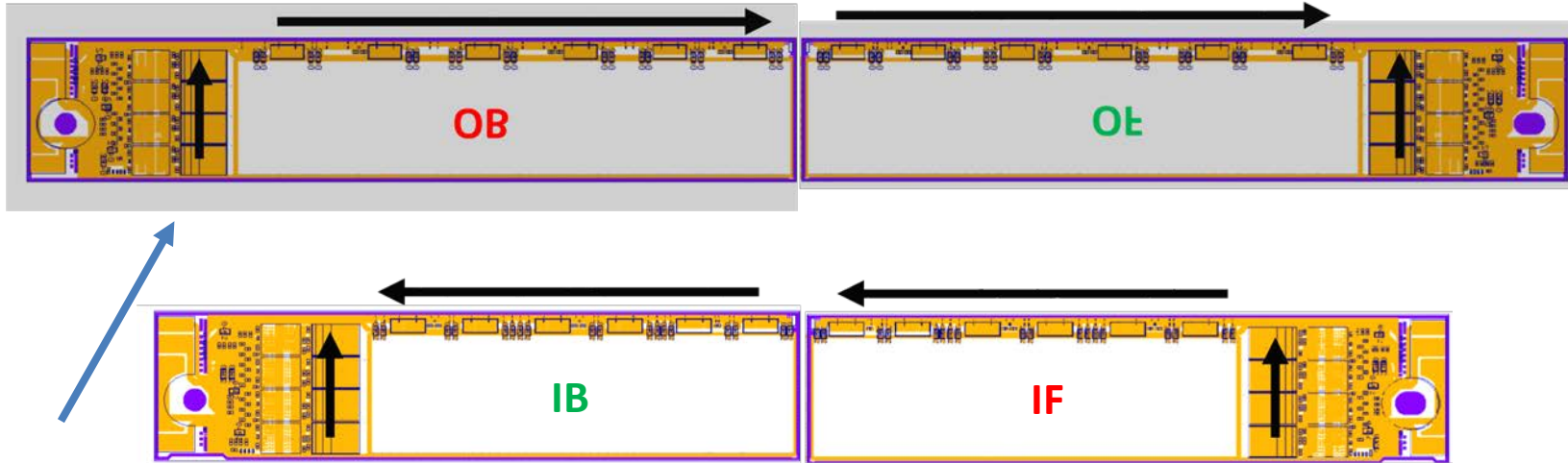


ASICs are “on top”
(looking on the
DEPFET side)

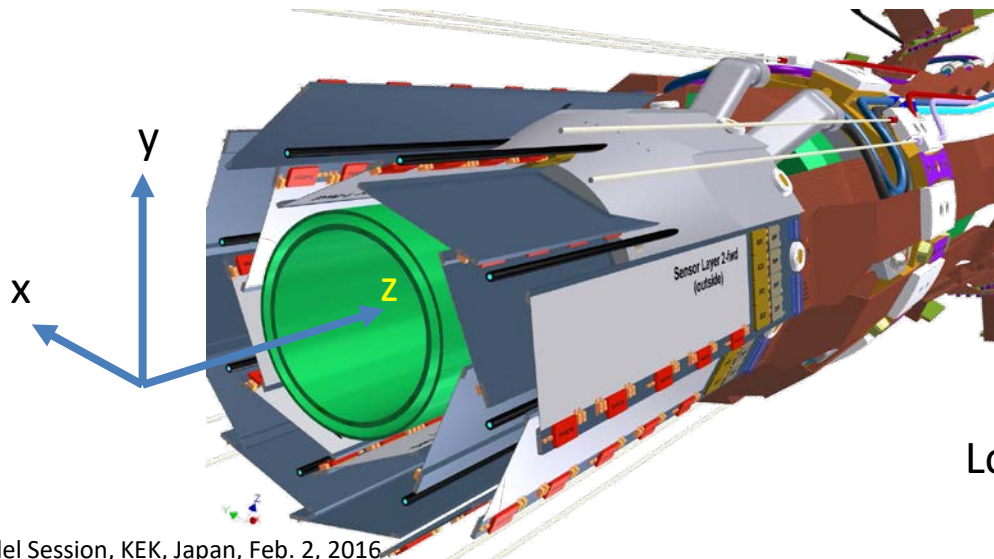
This would be my preferred
orientation of the layers
(from SVD towards IP)

Modules for the DESY Test

View from "inside" = in beam direction



View from "inside" = in beam direction



ASICs are "on bottom" (looking on the thinned side)

ASICs are "on top" (looking on the DEPFET side)

Looking from the IP