

Open Questions (C. Marinas)

- When could we ensure we can read 4 modules over 4 DHEs, 1 DHC, at 3% occupancy and 30 kHz trigger rate. With overlapping triggers. What do we need for that?
- Tests with PXD9-EMCM2 with DHPT1.2b with $\gg 3\%$ occupancy and 30 kHz trigger rate.
- Pedestals: Is there a procedure for this? For now, pedestal run. Interplay of BonnDAQ - DQM.
- Combined ROIs: HLT+DATCON at ONSSEN. Failure mode understood? What if data is coming corrupted or End Of Frame missing? Number of ROIs accepted by ONSSEN?
- Burn in tests (Burn-in – Test – Glue)
- DATCON: Final hardware (firmware!) When? How do we compare with HLT tracking? Execution time?
- PXD Clustering: Details about the implementation? Performance? Who is taking care of this (timeline)?
- Switcher: Long test stability tests
- Interlocks for lab set ups. How do we use the DHPT temperature sensor?