





Disclaimer

- In fact, I do not have anything new to present
- There was a final meeting of the Belle II GND/EMC group on 8 February 2017





Introduction

- GND/EMC concepts of SVD and PXD
 - Developed with Fernando Arteche (ITA Zaragoza, Spain)
 - And in consultation with the Belle II grounding group
 - Established since 2015
- EMC measurements were performed for both SVD (2015) and PXD (2016) in the anechoic chamber @ ITA
 - Improvements implemented after that for SVD
 - PXD was found to work well

M.Friedl: VXD GND/EMC





General Guidelines

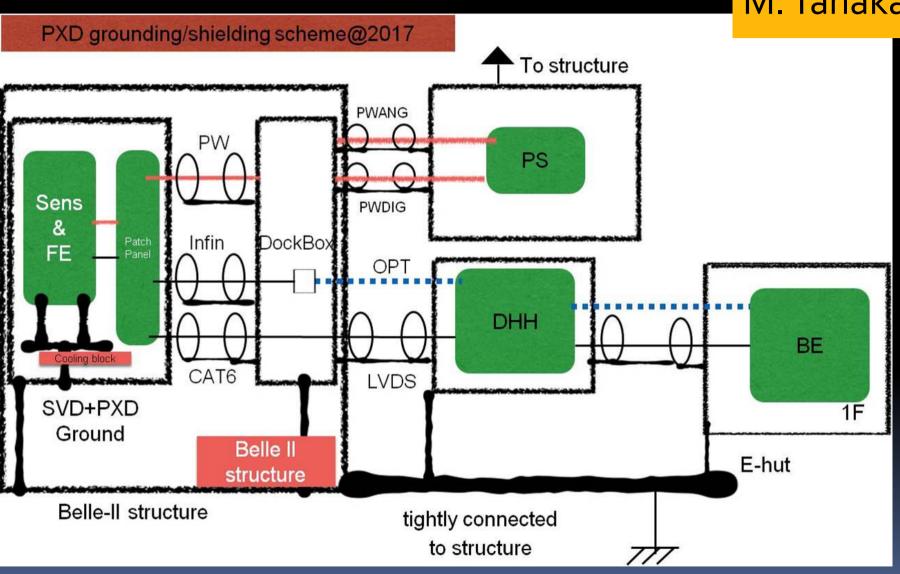
- All metallic pieces are to be grounded (not floating)
- VXD volume is electrically isolated from CDC and beam pipe
 - Cooling pipes are also separated by special isolators
 - Likewise, SVD DOCK boxes are isolated from CDC and grounded to end flanges of VXD
 - PXD is isolated from beam pipe
- BWD end flange is connected to solid Belle II structure
 - BWD and FWD sides are connected through metallized CF shell (SVD) or cooling tubes (PXD)
 - BWD and FWD end flanges/end rings become local ground points

M.Friedl: VXD GND/EMC



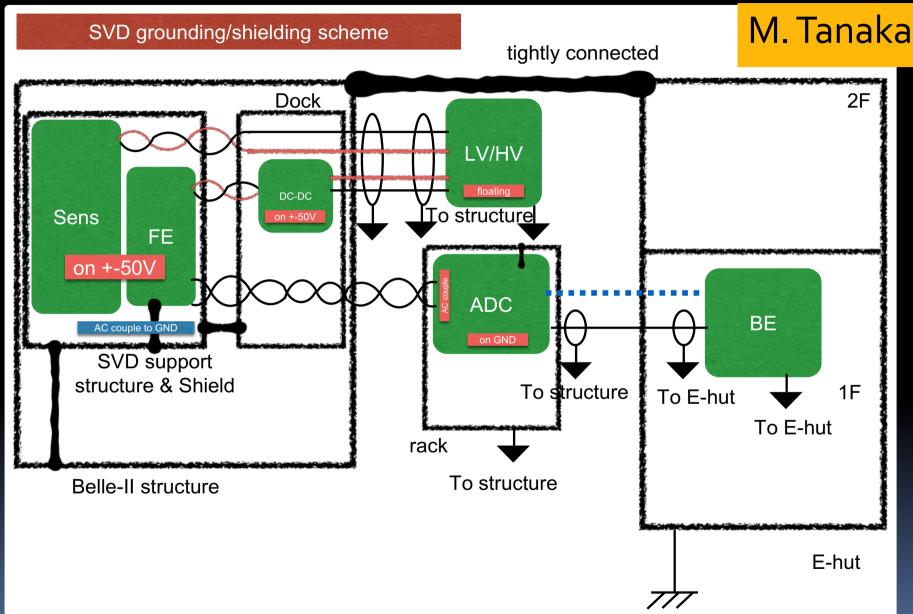


M. Tanaka





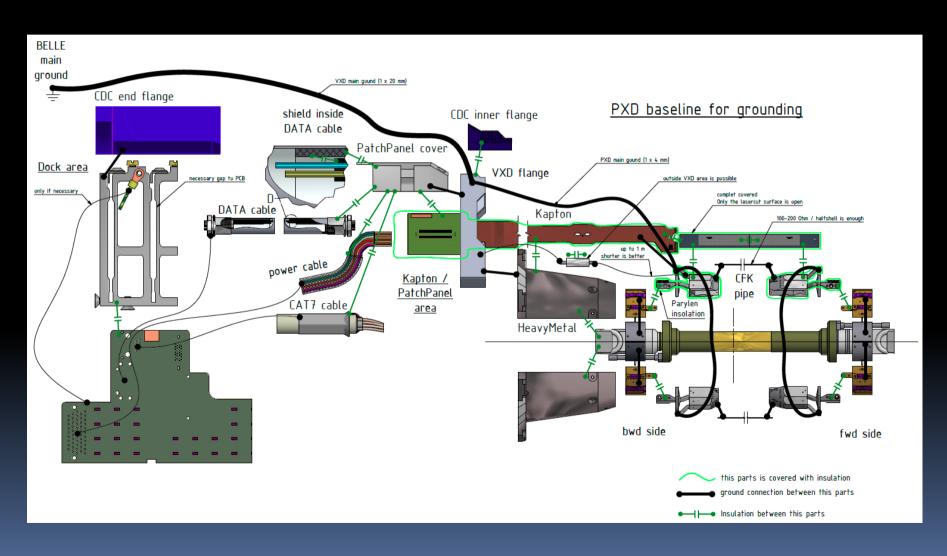








PXD Drawing







SVD Drawing

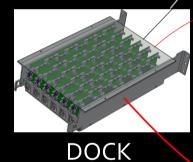
COPPER ~20m FADC (E-hut) (on top of Belle II)

> ~12m/ ~20m

LV & HV

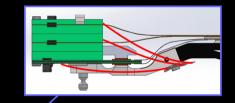
power supplies

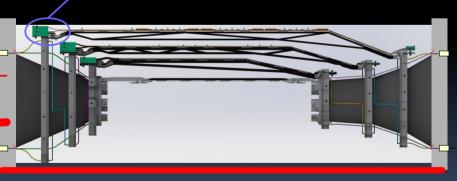
(on top/side of Belle II)



(isolated from CDC end wall) 2.5m

Red: cables with ground





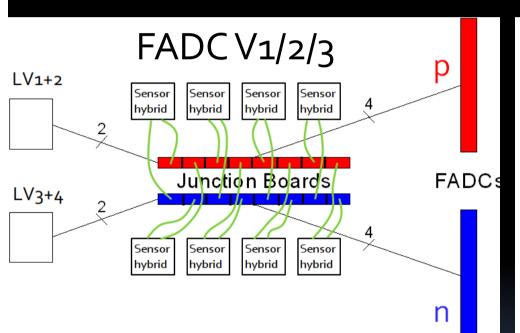
Ground is distributed from BW end flange to other places

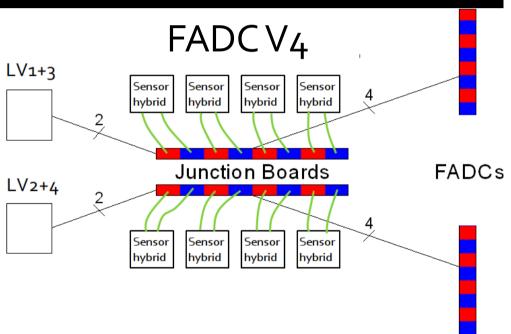




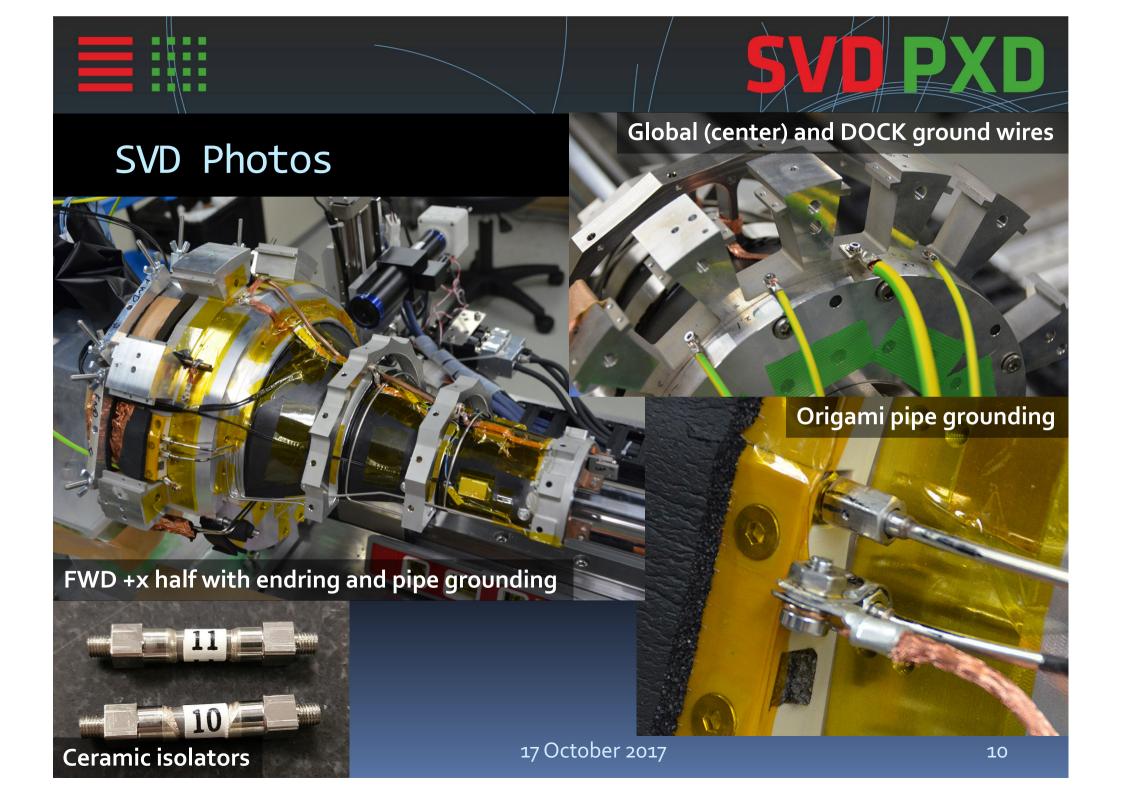
SVD: Mixed p/n Readout in V4

■ Up to V3: separate boards → potential ground loops





- V4: alternating p- and n-side readout on the same board
 - Performance: see yesterday's talk by Richard Thalmeier







Summary & Outlook

- Grounding scheme of PXD and SVD developed together Fernando Arteche and GND/EMC group
- Powering scheme with floating supplies and front-end ground reference
- EMC tests @ ITA Zaragoza (2015, 2016)
 - Leading to improvements for SVD
- Phase 2 will be the final test of the grounding scheme (implemented in the same way as in phase 3)

M.Friedl: VXD GND/EMC









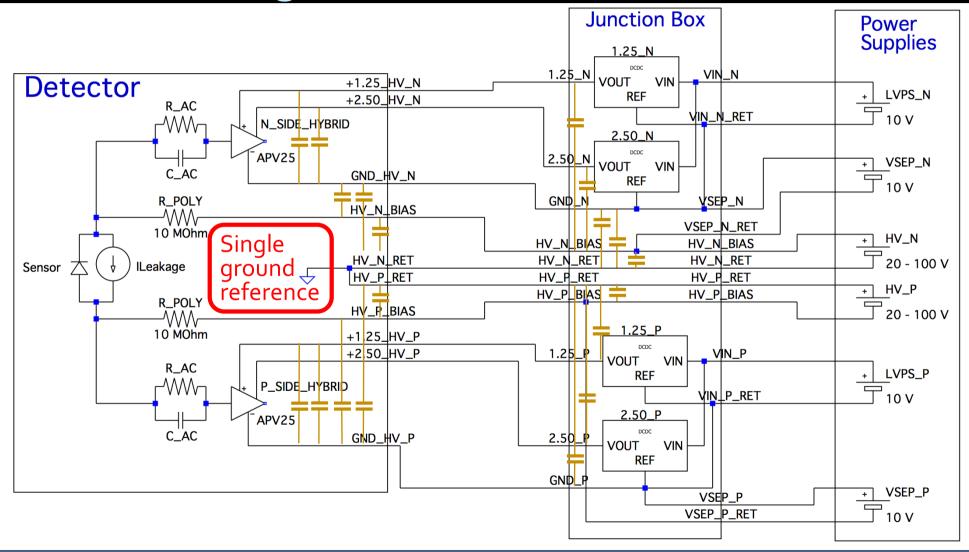


Backup Slides





SVD Powering Scheme

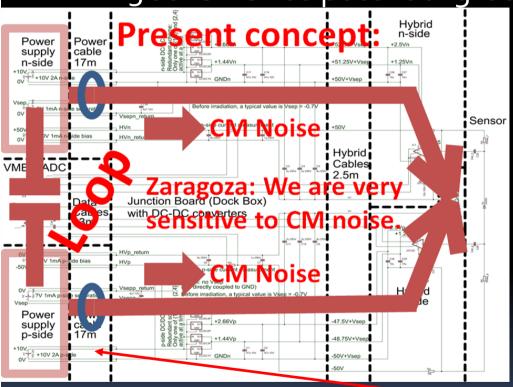


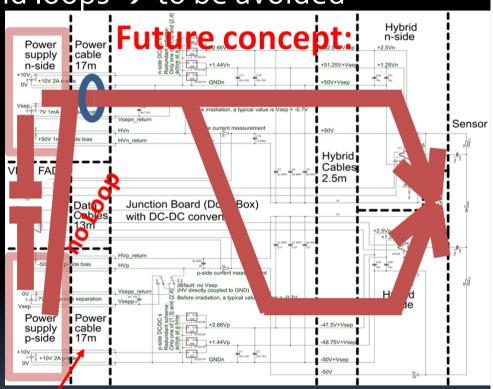




SVD FADC V3/V4

V3 scheme has potential ground loops -> to be avoided





Longest part of cable (17m) is here