

DHP

- MOSIS 90 nm submissions are in ~4 months intervals and sometimes cancelled due to not enough interest
- Oct. 26, 2009 submission was such a cancellation
- next ones
 - Feb 22, 2010
 - June 21, 2010
 - Oct. 25, 2010

- DCD has 64 outputs

option 1)

- submit DHP test chip with 16 inputs
- this may operate $\frac{1}{2}$ DCD with 2 DHPs
- connection could be either
 - wire bond pads
 - bump bond pads already ordered with C4 bumps
- expected timeline
 - 22.2.2010 submission of test chip
 - end April: chip back from foundry
 - end June: tested and bug fixes done
 - Oct. 25: submit full DHP chip
 - chip available January 2011
- use FPGA based DCD readout (like Manuel's board)
with DHP-functionality for fall 2010 matrix operations

option 2)

- try submission on Feb 22, 2010 with 32 inputs
- this is already close to the final chip with 2 DHP for 1 DCD
- disadvantage: costs and risk