

ILC VTX Workshop at Ringberg

Report of Contributions

Contribution ID: 1

Type: **not specified**

Welcome from the Organizers

Sunday, 28 May 2006 17:30 (10 minutes)

Primary author: LACI ANDRICEK, AXEL HOERMANN

Presenter: LACI ANDRICEK, AXEL HOERMANN

Session Classification: Welcome

Contribution ID: 2

Type: **not specified**

Precision Physics at the ILC

Sunday, 28 May 2006 17:40 (35 minutes)

Primary author: WOLFGANG HOLLIK

Presenter: WOLFGANG HOLLIK

Session Classification: Welcome

Contribution ID: 3

Type: **not specified**

Summary

Wednesday, 31 May 2006 11:00 (15 minutes)

Primary author: HANS-GUENTHER MOSER

Presenter: HANS-GUENTHER MOSER

Session Classification: Workshop Summary

Contribution ID: 4

Type: **not specified**

EUDET Status

Tuesday, 30 May 2006 16:00 (20 minutes)

Primary author: JOLANTA SZTUK

Presenter: JOLANTA SZTUK

Session Classification: Common Efforts- Discussion

Contribution ID: 5

Type: **not specified**

EMI Issue

Tuesday, 30 May 2006 16:20 (20 minutes)

Primary author: CHRIS DAMERELL

Presenter: CHRIS DAMERELL

Session Classification: Common Efforts- Discussion

Contribution ID: 6

Type: **not specified**

Discussion: EMI test plans, irradiation facilities, next steps..

Tuesday, 30 May 2006 16:40 (1h 20m)

Primary author: ALL (HANS-GUENTHER MOSER)

Presenter: ALL (HANS-GUENTHER MOSER)

Session Classification: Common Efforts- Discussion

Contribution ID: 7

Type: **not specified**

Machine related Background at the ILC VTX

Monday, 29 May 2006 16:00 (45 minutes)

Primary author: ADRIAN VOGEL

Presenter: ADRIAN VOGEL

Session Classification: ILC Environment

Contribution ID: 8

Type: **not specified**

Guided Tour through Ringberg Castle

Monday, 29 May 2006 17:00 (45 minutes)

Primary author: AXEL HOERMANN

Presenter: AXEL HOERMANN

Session Classification: ILC Environment

Contribution ID: 9

Type: **not specified**

Benchmarking the Vertex Tracker

Monday, 29 May 2006 09:00 (20 minutes)

Primary author: MARCO BATTAGLIA

Presenter: MARCO BATTAGLIA

Session Classification: Physics Requirements/Simulation

Contribution ID: **10**

Type: **not specified**

Jet Flavour tagging

Monday, 29 May 2006 09:20 (20 minutes)

Primary author: THORSTEN KUHL

Presenter: THORSTEN KUHL

Session Classification: Physics Requirements/Simulation

Contribution ID: **11**

Type: **not specified**

Implementing a realistic VTX Geometry in G4

Monday, 29 May 2006 10:00 (20 minutes)

Primary author: DAMIEN GRANDJEAN

Presenter: DAMIEN GRANDJEAN

Session Classification: Physics Requirements/Simulation

Contribution ID: 12

Type: **not specified**

The VTX detector in the ILC software framework

Monday, 29 May 2006 09:40 (20 minutes)

Primary author: FRANK GAEDE

Presenter: FRANK GAEDE

Session Classification: Physics Requirements/Simulation

Contribution ID: **13**

Type: **not specified**

The Vertex Detector in the SiD Concept

Monday, 29 May 2006 11:30 (20 minutes)

Primary author: BILL COOPER

Presenter: BILL COOPER

Session Classification: Simulation/Detector Concepts

Contribution ID: **14**

Type: **not specified**

The Vertex Detector in the LDC Concept

Monday, 29 May 2006 11:50 (20 minutes)

Primary author: SONJA HILLERT

Presenter: SONJA HILLERT

Session Classification: Simulation/Detector Concepts

Contribution ID: 15

Type: **not specified**

The Vertex Detector in the GLD Concept

Monday, 29 May 2006 12:10 (20 minutes)

Primary author: YASUHIRO SUGIMOTO

Presenter: YASUHIRO SUGIMOTO

Session Classification: Simulation/Detector Concepts

Contribution ID: **16**

Type: **not specified**

DEPFET Pixel sensors simulation in Marlin

Monday, 29 May 2006 10:50 (20 minutes)

Primary author: ALEXEI RASPEREZA

Presenter: ALEXEI RASPEREZA

Session Classification: Simulation/Detector Concepts

Contribution ID: 17

Type: **not specified**

CMOS Pixel sensors simulation in Marlin

Monday, 29 May 2006 11:10 (20 minutes)

Primary author: MARCO BATTAGLIA

Presenter: MARCO BATTAGLIA

Session Classification: Simulation/Detector Concepts

Contribution ID: **18**

Type: **not specified**

ALICE Vertex Detector

Monday, 29 May 2006 14:30 (30 minutes)

Primary author: PETRA RIEDLER

Presenter: PETRA RIEDLER

Session Classification: Integration - Interconnection

Contribution ID: 19

Type: **not specified**

Vertex Ladder Engineering: from the STAR experience to an ILC design

Monday, 29 May 2006 15:00 (20 minutes)

Primary author: DEVIS CONTARATO

Presenter: DEVIS CONTARATO

Session Classification: Integration - Interconnection

Contribution ID: **20**

Type: **not specified**

VTX Mechanical Support Structure

Monday, 29 May 2006 14:00 (30 minutes)

Primary author: BILL COOPER

Presenter: BILL COOPER

Session Classification: Integration - Interconnection

Contribution ID: **21**

Type: **not specified**

CP-CCD and ISIS

Tuesday, 30 May 2006 09:00 (45 minutes)

Primary author: KONSTANTIN STEFANOV - JOEL GOLDSTEIN

Presenter: KONSTANTIN STEFANOV - JOEL GOLDSTEIN

Session Classification: Pixel Technologies I

Contribution ID: 22

Type: **not specified**

DEPFET for the ILC VTX

Tuesday, 30 May 2006 09:45 (45 minutes)

Primary author: RAINER RICHTER - HANS KRUEGER

Presenter: RAINER RICHTER - HANS KRUEGER

Session Classification: Pixel Technologies I

Contribution ID: **23**

Type: **not specified**

FP-CCD

Tuesday, 30 May 2006 14:00 (30 minutes)

Primary author: TADASHI NAGAMINE

Presenter: TADASHI NAGAMINE

Session Classification: Pixel Technologies III

Contribution ID: 24

Type: **not specified**

SOI and 3D Integrated Sensors

Tuesday, 30 May 2006 14:30 (30 minutes)

Primary author: RAY YAREMA

Presenter: RAY YAREMA

Session Classification: Pixel Technologies III

Contribution ID: 25

Type: **not specified**

SOI (Sucima)

Tuesday, 30 May 2006 15:00 (30 minutes)

Primary author: WOJCIECH KUCEWICZ

Presenter: WOJCIECH KUCEWICZ

Session Classification: Pixel Technologies III

Contribution ID: 26

Type: **not specified**

MIMOSA and FAPS

Tuesday, 30 May 2006 11:00 (45 minutes)

Primary author: MARC WINTER

Presenter: MARC WINTER

Session Classification: Pixel Technologies II

Contribution ID: 27

Type: **not specified**

CMOS MAPS with hybrid-pixel-like analog readout electronics in a 130 nm process

Tuesday, 30 May 2006 12:05 (20 minutes)

Primary author: VALERIO RE

Presenter: VALERIO RE

Session Classification: Pixel Technologies II

Contribution ID: **28**

Type: **not specified**

CMOS R&D at LBNL

Tuesday, 30 May 2006 11:45 (20 minutes)

Primary author: DEVIS CONTARATO

Presenter: DEVIS CONTARATO

Session Classification: Pixel Technologies II

Contribution ID: 29

Type: **not specified**

Discussion: Outline of the "White Paper", Contributions, Editors?

Wednesday, 31 May 2006 09:00 (1h 30m)

Session Classification: Discussion