



First Measurements: DTC2



Jochen Knopf
jochen.knopf@ziti.uni-heidelberg.de
PXD EVO-Meeting
23.03.2010

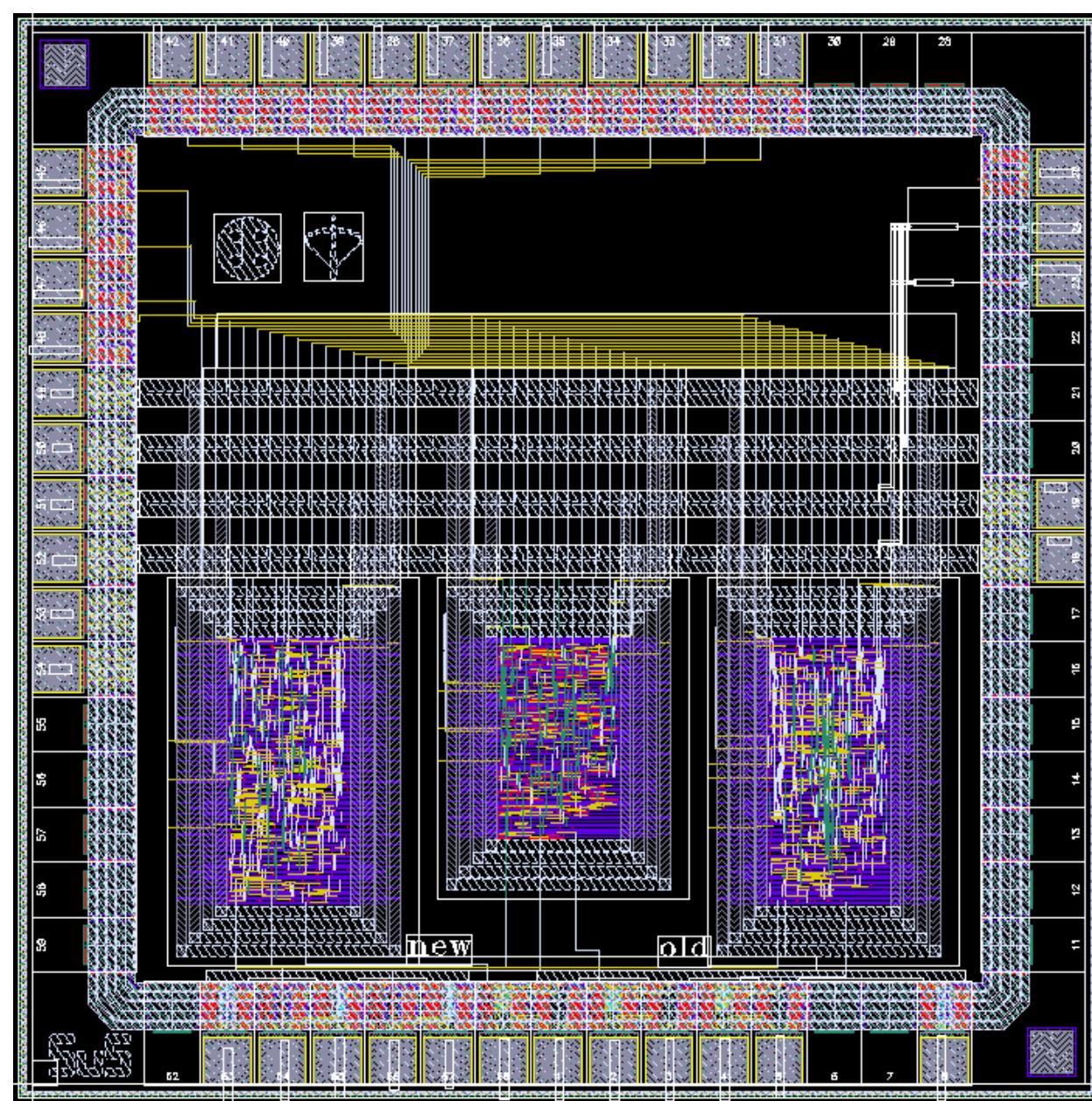
Digital Test Chip (DTC2)

Area: 1 MiniASIC

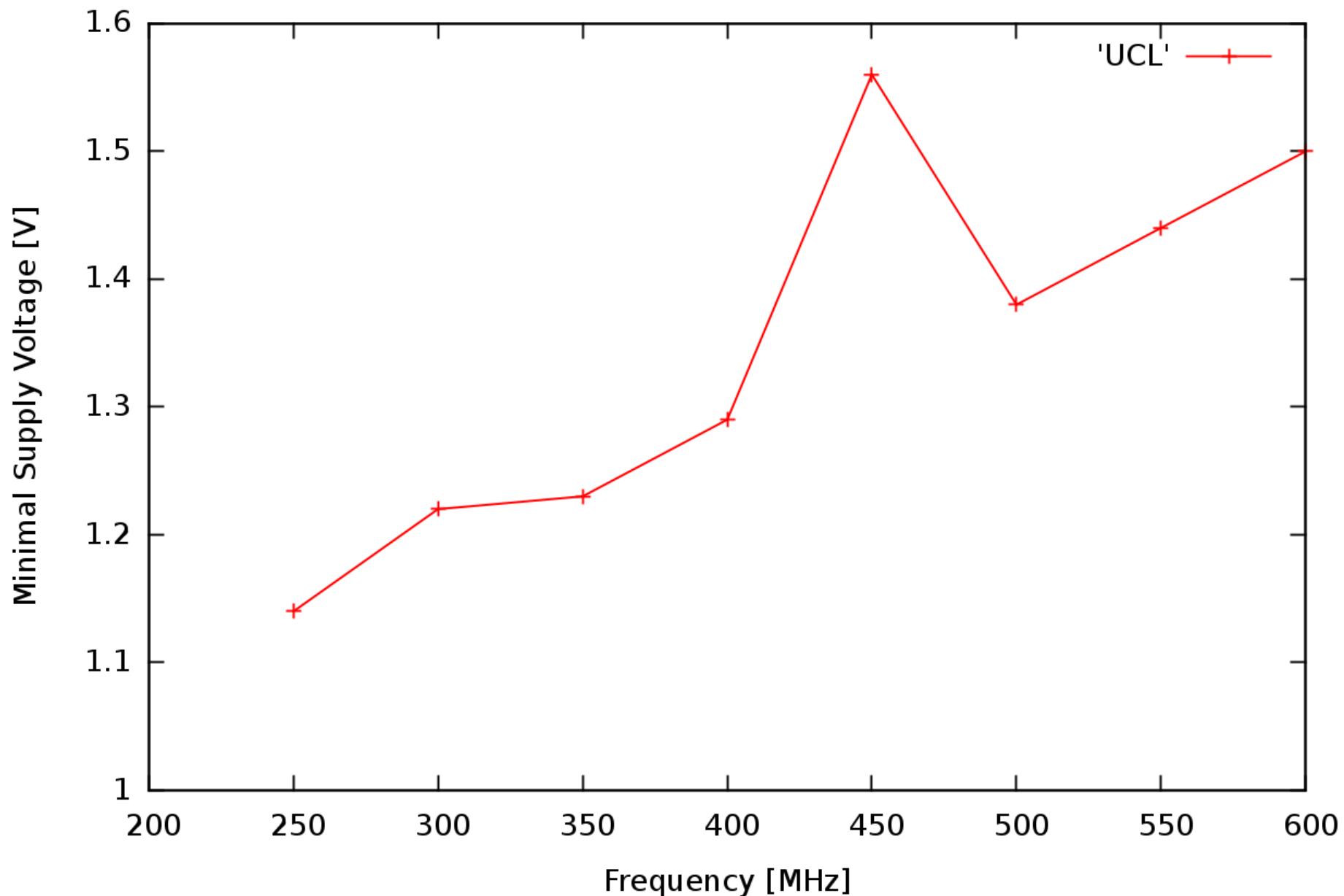
Contains DCDB-like logic blocks, synthesized with three different standard cell libraries

Purpose:

- Verifying DCDB logic
- Testing standard cells
- Verifying work flow



First Measurement Results



Thank you!