PXD Data Acquisition Workshop

Mission:

- Prepare the material for a decision on the PXD DAQ System
- Data of PXD must be reduced by about a factor of 10
- Method: Use „outer“ detector information (SVD, CDC)
  to define „ROIs“ for PXD data selection
- At present there are several options (see B2GM at KEK):
  IHEP/Giessen system:
  „real-time“: full algorithm on CN
  „pipeline“: Storage and Event Building
  fall-back: PC-based solution

Thursday 22 April 2010

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08:00	Breakfast (45')	
09:00	Welcome and Introduction (15')	Christian Kiesling
09:15	Algorithms for PXD Data Reduction (45')	Christian Kiesling
10:00	Introduction to the DHH System (30')	Igor Konorov
10:30	Coffee break (15')	
10:45	Overview on SVD and its DAQ System (30')	Markus Friedl
11:15	The IHEP/Giessen ATCA System (45')	Sören Lange
12:00	The PC Readout Option (30')	Takeo Higuchi
12:30	Multi-Core Processors for PXD Data Reduction Algorithms (20')	Kolja Prothmann
13:00	Lunch break (1h00')	
14:00	Trigger Timing Disitribution (30')	Mikihiko Nakao
14:30	The High Level Trigger Framework (30')	Ryosuke Itoh
15:00	Integration of the PXD Data Acquisition (30')	Takeo Higuchi
15:30	Proposal for PXD-DAQ Document (15')	Takeo Higuchi
15:45	Coffee break (15')	
16:00	Discussion on Protocol between DHP/DHH and Trigger (1h30')	
18:30	Dinner (1h30')	

Friday 23 April 2010

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08:00	Breakfast (45')
09:00	Pick-up and Discussion of "Left Overs" from Day One (45')
09:45	Relation between PXD DAQ and the HLT (1h30')
11:15	Coffee break (15')
11:30	Possible Scenarios for the ATCA System (I) (1h30')
13:00	Lunch (1h00')
14:00	Possible Scenarios for the ATCA System (II) (1h00')
15:00	Summary and Conclusions (20')
15:30	Coffee and Departure (30')