

4th International Workshop on DEPFET Detectors and Applications

Monday, 3 May 2010

ASIC Design and Testresults - Lecture Room (14:00 - 16:00)

time	[id] title	presenter
14:00	[39] Switcher Design Summary	Prof. FISCHER, Peter
14:10	[43] DCD-RO Status	Mr KREIDL, Christian
14:25	[41] DCD Status	Mr KNOPF, Jochen
14:40	[44] DHP Design Summary	KRÜGER, Hans
14:55	[42] DCE8 Status	Dr WASSATSCH, Andreas