

5th International Workshop on DEPFET Detectors and Applications

Wednesday, 29 September 2010

ASIC Design and test results (09:15 - 10:45)

time	[id] title	presenter
09:15	[19] Switcher B	KREIDL, Christian
09:30	[18] DCDB measurement updates	KNOPF, Jochen
09:45	[20] DHP 0.1 Status	HEMPEREK, Tomasz
10:00	[23] DHP 0.1 Analog Blocks	COMERMA, Albert
10:15	[21] Clustering Implementation "dce8"	WASSATSCH, Andreas
10:30	[22] DHP Simulation	KISHISHITA, Tetsuichi
10:40	[48] Signal resolution and dynamic range	KOCH, Manuel