\*\*\* AGENDA \*\*\*

1. Vertex Tracking and performance
2. DEPFET Pixel Cell
3. ASIC Design and test result
	1. DHP algorithm
4. Interface to Belle II and detector operation
	1. Background simulation
	2. Slow control (will this be part of a Belle II global system?)
	3. Calibration needs and DAQ modes/databases
5. Interconnection/ Module Design /DHH
	1. Finalize parameters (number of switcher lines/DCD channels)
6. Engineering ( Cooling & Mechanics, services ?)
7. Test systems
8. Test Beams
	1. discussion of TB paper
9. Wrap-up session and AOB

Thursday evening (IB meeting)