

LMU - Cluster Universe Stefan Rummel

PS meeting - Sept. 2nd







- Ivan on DCD/DHP regulation requirements
- News from Santjago
- LMU news, regulation in presence of delays
- Discussion:
 - Source on GND / grounding
 - Parameter definition and ranges
 - Open issue Over voltage protection





- Service:
 - TDR cable samples shipped to Japan

LMU

- In contact with Samtec for application specific connector for Gbit links on a width*profile of 20*5.7mm
- Just received some infos regarding: B-field and radiation hardness outside the detector
 - Order of magnitude ~10G (~mT) outside the flux return yoke
 - Outer layer of KLM sees a rate of 0.48 Hz/cm2 → back on the envelope calculation gives - .1kRad/10a with factor 100 safty
- Dipl. Ing. Andreas Seiler joined our group on August 15th
- Started to investigate regulation over long distance
 - Design of a regulator and a test-load with fast transients
 - Analysis of regulation with delays with Matlab



•Goals:

- Understand impact of long lines
- Get idea of limits ↔ requierments of ASIC's
- Get idea on requiered capacitors capacity, ESR important feedback to EOM- and/or PP- design



Simulation of regulator response with delays
Delay at least L/c c~0.6c0 → 55ns for 10m cable
Investigated model:



Figure of merit:

- Step response
- Settling time, over shoot, rise time...
- Phase margin







Next steps:

- Incorporate realistic parasitics Rline, Lline, Cpass..
- Investigate stability in Cload, ESR, speed parameter space
- Test with realistic cable (length, diameter)